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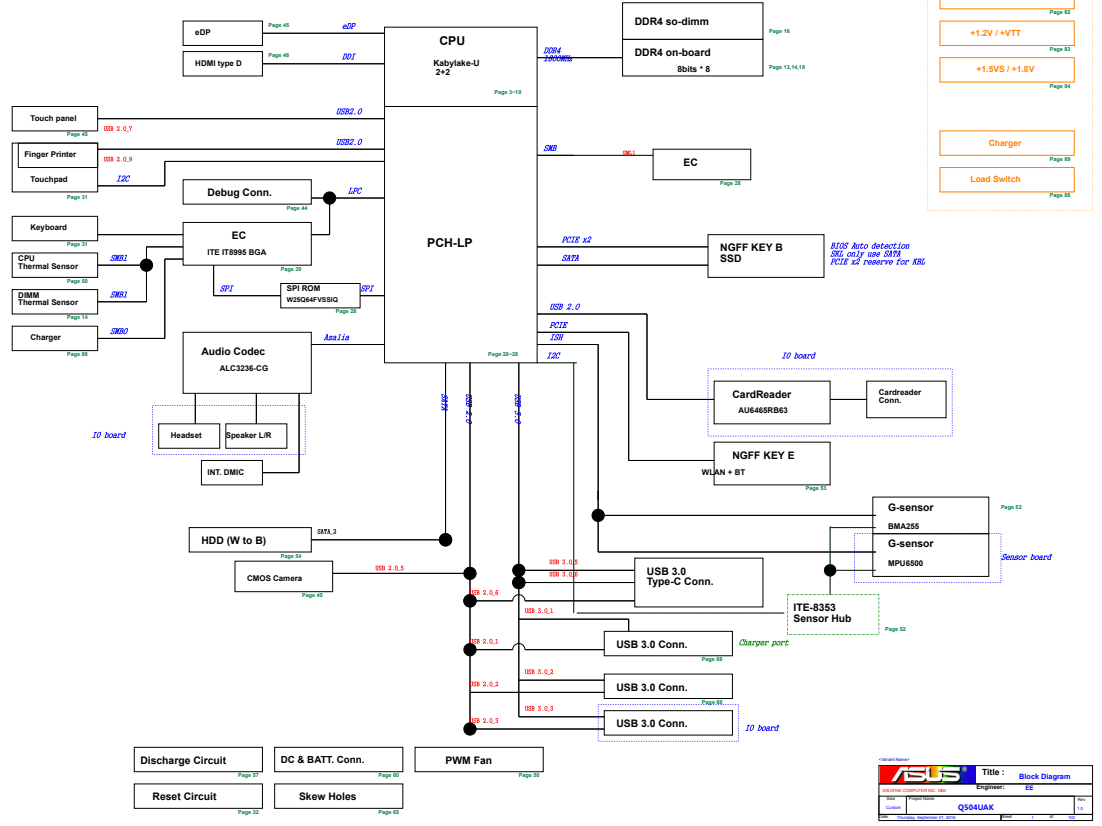
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Q504UAK SCHEMATIC Revision 0.1

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PCH_CPT
GPIO

绿色字體: 預留路徑, 電阻沒上件

Pin Name	Config	Signal Name	Default Value	Ref Pin Name	Value
GPIO_01	GPIO	GPIO_01	GPIO_01	GPIO_01	GPIO_01
GPIO_02	GPIO	GPIO_02	GPIO_02	GPIO_02	GPIO_02
GPIO_03	GPIO	GPIO_03	GPIO_03	GPIO_03	GPIO_03
GPIO_04	GPIO	GPIO_04	GPIO_04	GPIO_04	GPIO_04
GPIO_05	GPIO	GPIO_05	GPIO_05	GPIO_05	GPIO_05
GPIO_06	GPIO	GPIO_06	GPIO_06	GPIO_06	GPIO_06
GPIO_07	GPIO	GPIO_07	GPIO_07	GPIO_07	GPIO_07
GPIO_08	GPIO	GPIO_08	GPIO_08	GPIO_08	GPIO_08
GPIO_09	GPIO	GPIO_09	GPIO_09	GPIO_09	GPIO_09
GPIO_10	GPIO	GPIO_10	GPIO_10	GPIO_10	GPIO_10
GPIO_11	GPIO	GPIO_11	GPIO_11	GPIO_11	GPIO_11
GPIO_12	GPIO	GPIO_12	GPIO_12	GPIO_12	GPIO_12
GPIO_13	GPIO	GPIO_13	GPIO_13	GPIO_13	GPIO_13
GPIO_14	GPIO	GPIO_14	GPIO_14	GPIO_14	GPIO_14
GPIO_15	GPIO	GPIO_15	GPIO_15	GPIO_15	GPIO_15
GPIO_16	GPIO	GPIO_16	GPIO_16	GPIO_16	GPIO_16
GPIO_17	GPIO	GPIO_17	GPIO_17	GPIO_17	GPIO_17
GPIO_18	GPIO	GPIO_18	GPIO_18	GPIO_18	GPIO_18
GPIO_19	GPIO	GPIO_19	GPIO_19	GPIO_19	GPIO_19
GPIO_20	GPIO	GPIO_20	GPIO_20	GPIO_20	GPIO_20
GPIO_21	GPIO	GPIO_21	GPIO_21	GPIO_21	GPIO_21
GPIO_22	GPIO	GPIO_22	GPIO_22	GPIO_22	GPIO_22
GPIO_23	GPIO	GPIO_23	GPIO_23	GPIO_23	GPIO_23
GPIO_24	GPIO	GPIO_24	GPIO_24	GPIO_24	GPIO_24
GPIO_25	GPIO	GPIO_25	GPIO_25	GPIO_25	GPIO_25
GPIO_26	GPIO	GPIO_26	GPIO_26	GPIO_26	GPIO_26
GPIO_27	GPIO	GPIO_27	GPIO_27	GPIO_27	GPIO_27
GPIO_28	GPIO	GPIO_28	GPIO_28	GPIO_28	GPIO_28
GPIO_29	GPIO	GPIO_29	GPIO_29	GPIO_29	GPIO_29
GPIO_30	GPIO	GPIO_30	GPIO_30	GPIO_30	GPIO_30
GPIO_31	GPIO	GPIO_31	GPIO_31	GPIO_31	GPIO_31
GPIO_32	GPIO	GPIO_32	GPIO_32	GPIO_32	GPIO_32
GPIO_33	GPIO	GPIO_33	GPIO_33	GPIO_33	GPIO_33
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GPIO_35	GPIO	GPIO_35	GPIO_35	GPIO_35	GPIO_35
GPIO_36	GPIO	GPIO_36	GPIO_36	GPIO_36	GPIO_36
GPIO_37	GPIO	GPIO_37	GPIO_37	GPIO_37	GPIO_37
GPIO_38	GPIO	GPIO_38	GPIO_38	GPIO_38	GPIO_38
GPIO_39	GPIO	GPIO_39	GPIO_39	GPIO_39	GPIO_39
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GPIO_42	GPIO	GPIO_42	GPIO_42	GPIO_42	GPIO_42
GPIO_43	GPIO	GPIO_43	GPIO_43	GPIO_43	GPIO_43
GPIO_44	GPIO	GPIO_44	GPIO_44	GPIO_44	GPIO_44
GPIO_45	GPIO	GPIO_45	GPIO_45	GPIO_45	GPIO_45
GPIO_46	GPIO	GPIO_46	GPIO_46	GPIO_46	GPIO_46
GPIO_47	GPIO	GPIO_47	GPIO_47	GPIO_47	GPIO_47
GPIO_48	GPIO	GPIO_48	GPIO_48	GPIO_48	GPIO_48
GPIO_49	GPIO	GPIO_49	GPIO_49	GPIO_49	GPIO_49
GPIO_50	GPIO	GPIO_50	GPIO_50	GPIO_50	GPIO_50
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GPIO_52	GPIO	GPIO_52	GPIO_52	GPIO_52	GPIO_52
GPIO_53	GPIO	GPIO_53	GPIO_53	GPIO_53	GPIO_53
GPIO_54	GPIO	GPIO_54	GPIO_54	GPIO_54	GPIO_54
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GPIO_56	GPIO	GPIO_56	GPIO_56	GPIO_56	GPIO_56
GPIO_57	GPIO	GPIO_57	GPIO_57	GPIO_57	GPIO_57
GPIO_58	GPIO	GPIO_58	GPIO_58	GPIO_58	GPIO_58
GPIO_59	GPIO	GPIO_59	GPIO_59	GPIO_59	GPIO_59
GPIO_60	GPIO	GPIO_60	GPIO_60	GPIO_60	GPIO_60
GPIO_61	GPIO	GPIO_61	GPIO_61	GPIO_61	GPIO_61
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GPIO_73	GPIO	GPIO_73	GPIO_73	GPIO_73	GPIO_73
GPIO_74	GPIO	GPIO_74	GPIO_74	GPIO_74	GPIO_74
GPIO_75	GPIO	GPIO_75	GPIO_75	GPIO_75	GPIO_75
GPIO_76	GPIO	GPIO_76	GPIO_76	GPIO_76	GPIO_76
GPIO_77	GPIO	GPIO_77	GPIO_77	GPIO_77	GPIO_77
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GPIO_83	GPIO	GPIO_83	GPIO_83	GPIO_83	GPIO_83
GPIO_84	GPIO	GPIO_84	GPIO_84	GPIO_84	GPIO_84
GPIO_85	GPIO	GPIO_85	GPIO_85	GPIO_85	GPIO_85
GPIO_86	GPIO	GPIO_86	GPIO_86	GPIO_86	GPIO_86
GPIO_87	GPIO	GPIO_87	GPIO_87	GPIO_87	GPIO_87
GPIO_88	GPIO	GPIO_88	GPIO_88	GPIO_88	GPIO_88
GPIO_89	GPIO	GPIO_89	GPIO_89	GPIO_89	GPIO_89
GPIO_90	GPIO	GPIO_90	GPIO_90	GPIO_90	GPIO_90
GPIO_91	GPIO	GPIO_91	GPIO_91	GPIO_91	GPIO_91
GPIO_92	GPIO	GPIO_92	GPIO_92	GPIO_92	GPIO_92
GPIO_93	GPIO	GPIO_93	GPIO_93	GPIO_93	GPIO_93
GPIO_94	GPIO	GPIO_94	GPIO_94	GPIO_94	GPIO_94
GPIO_95	GPIO	GPIO_95	GPIO_95	GPIO_95	GPIO_95
GPIO_96	GPIO	GPIO_96	GPIO_96	GPIO_96	GPIO_96
GPIO_97	GPIO</				

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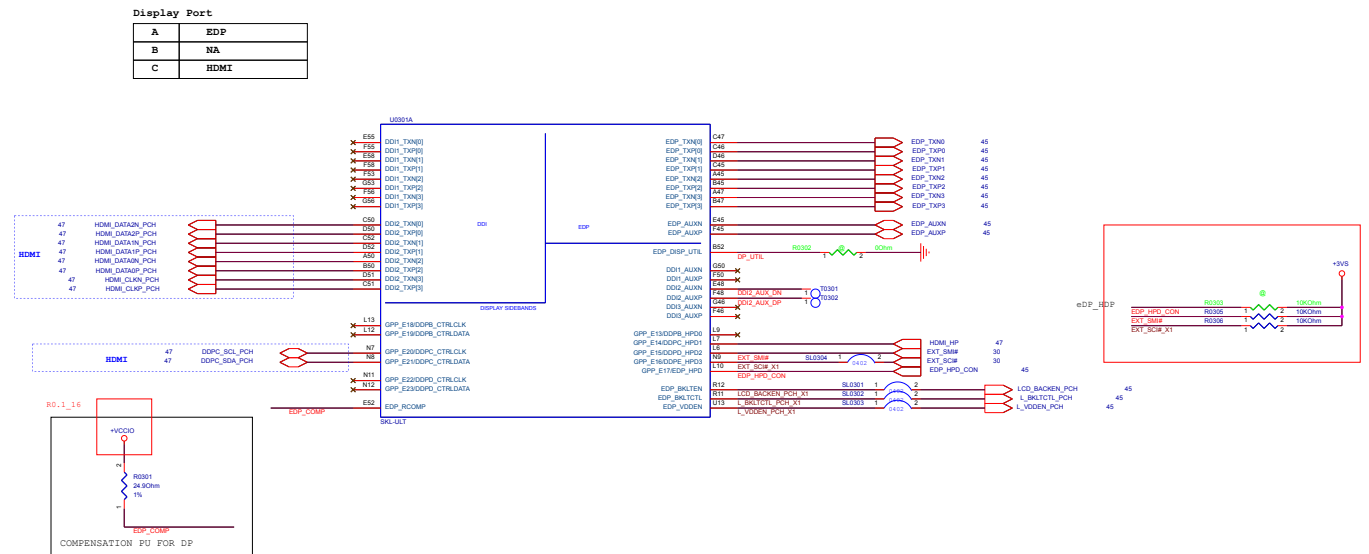
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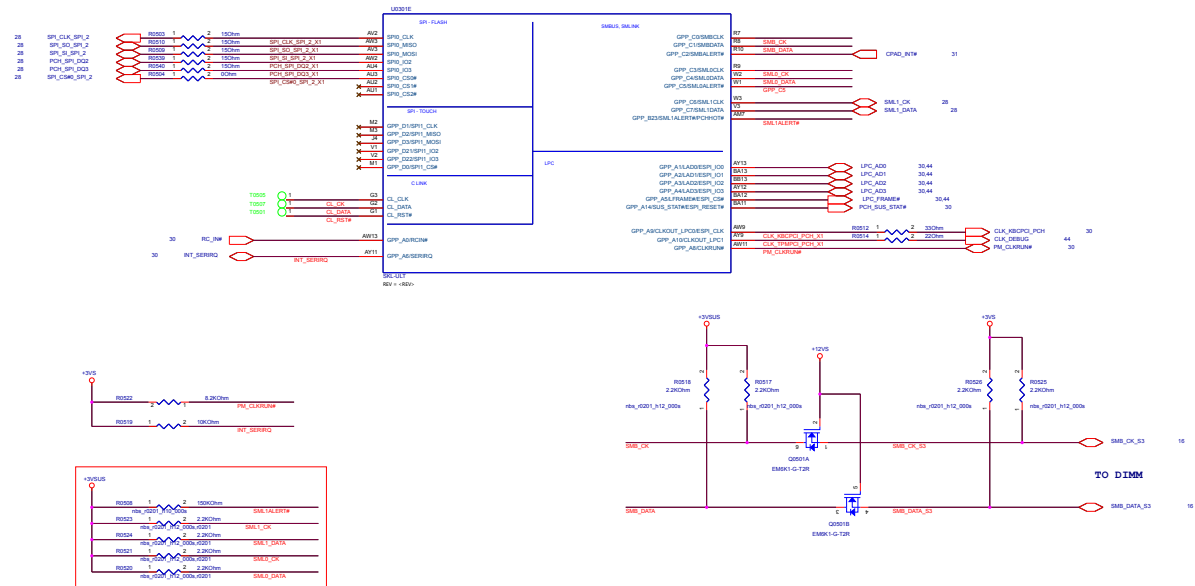
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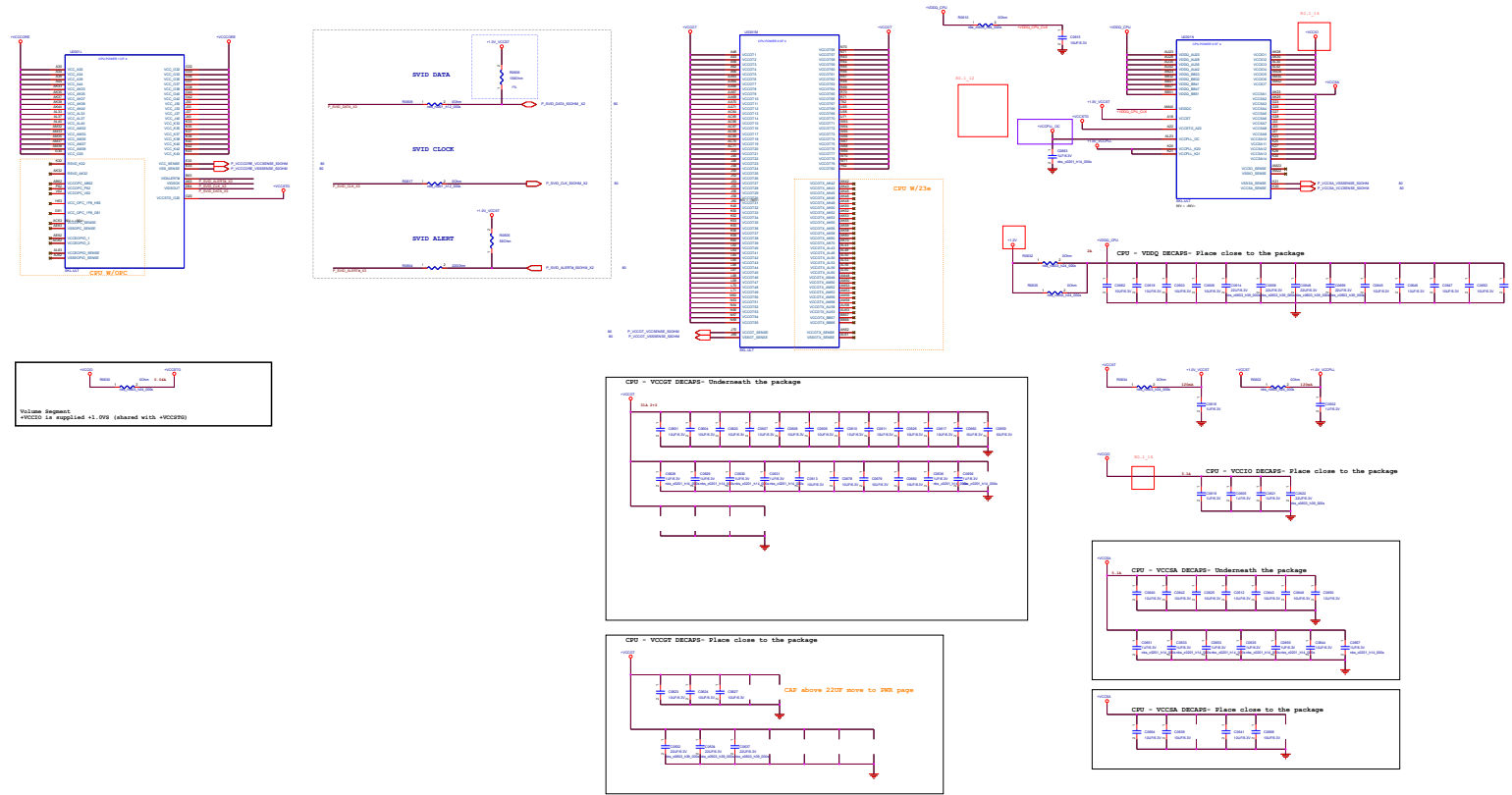
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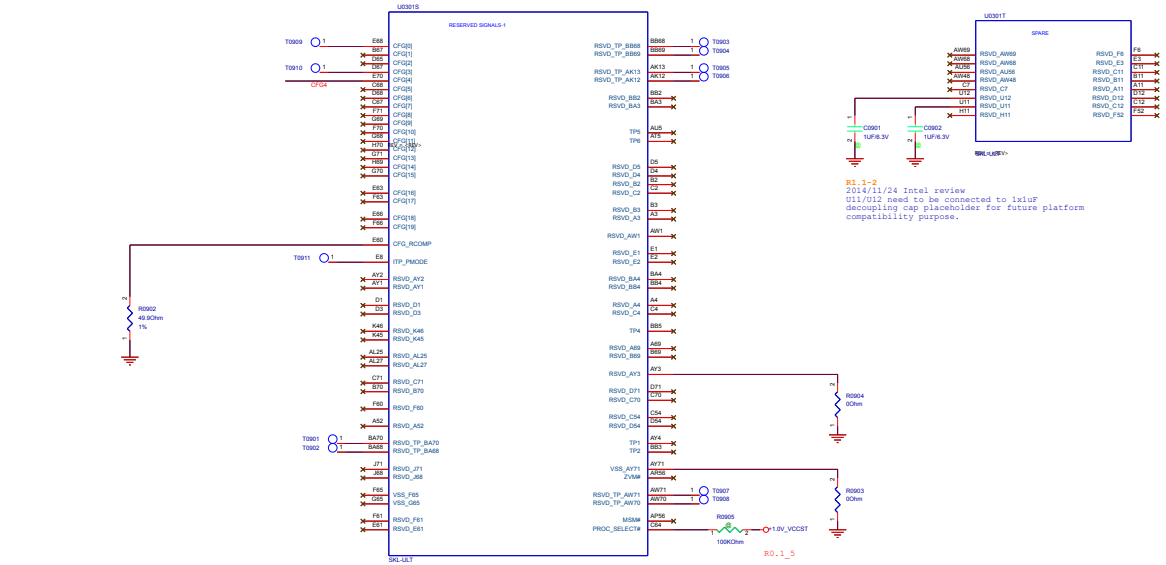
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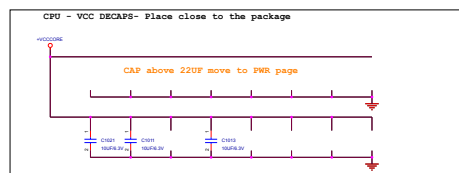
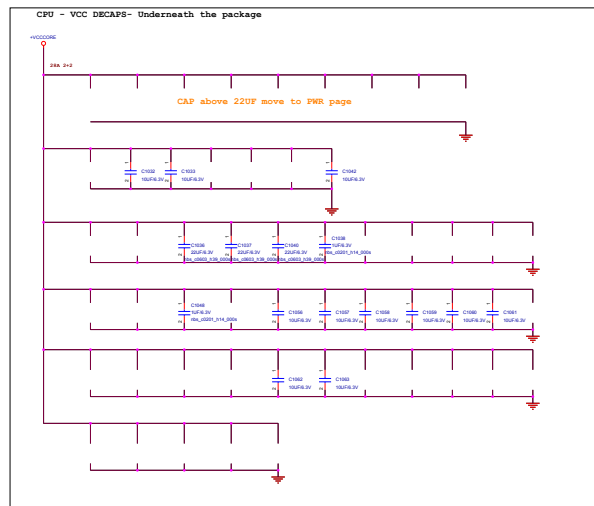
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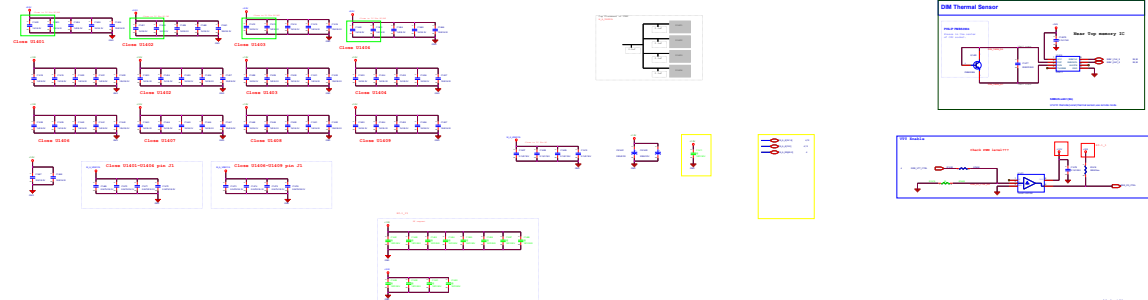
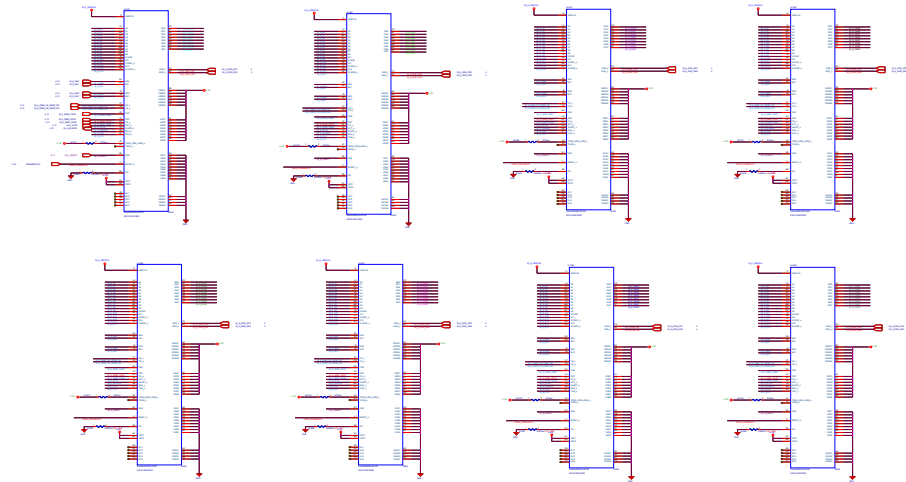
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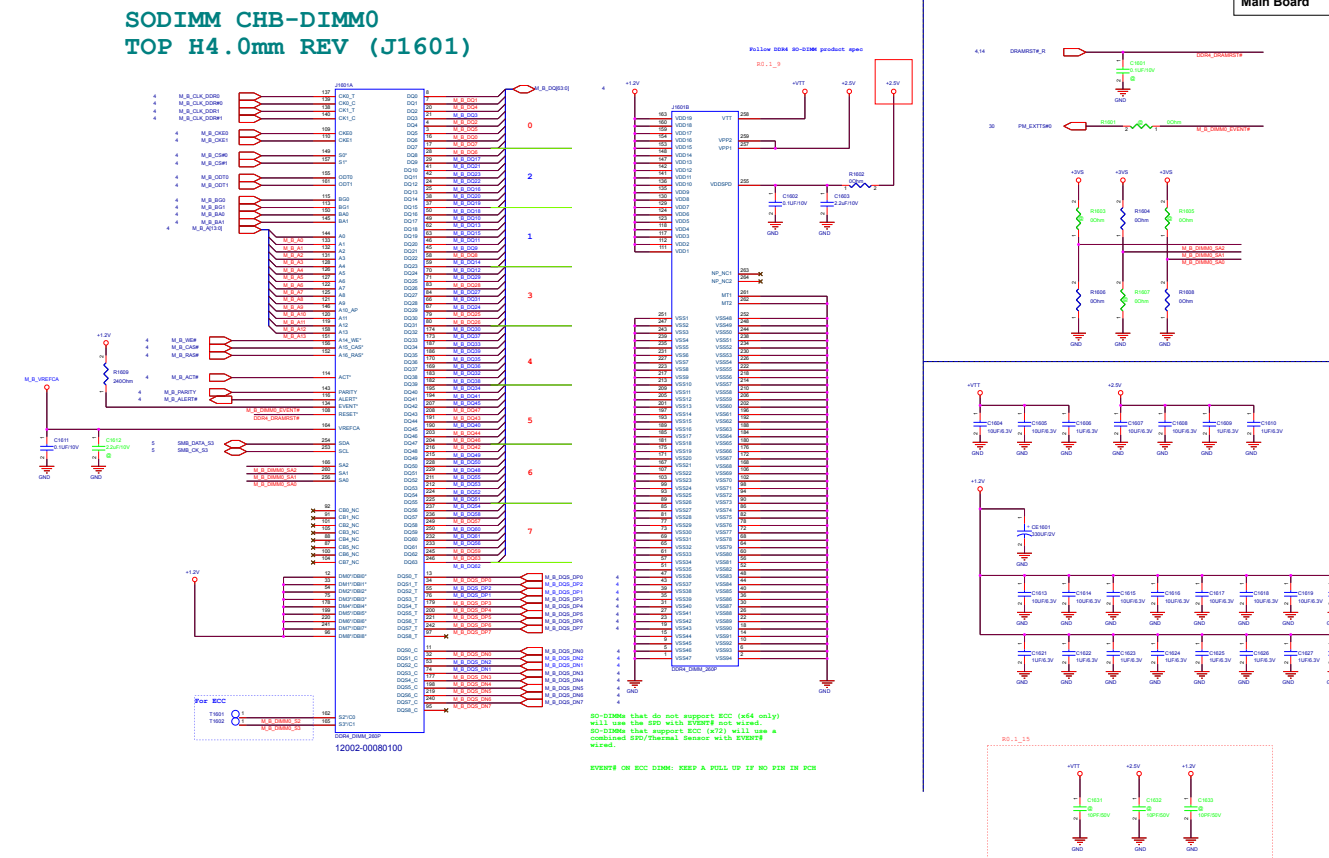
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Figure 4-51. SKL U DDR4/-RS x8 Devices Memory Down V_{REF-CA} Overview

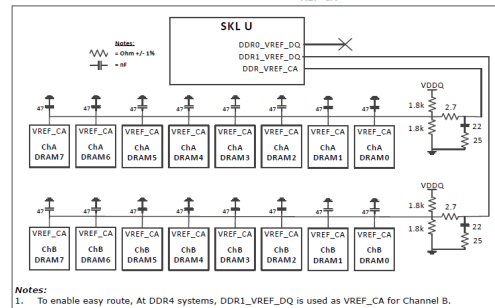
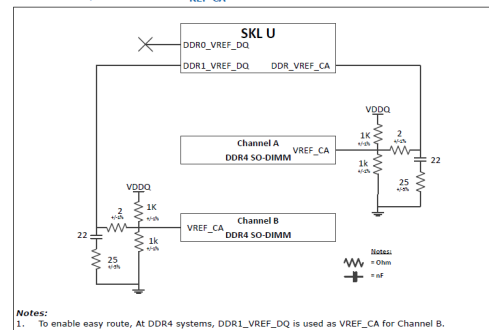
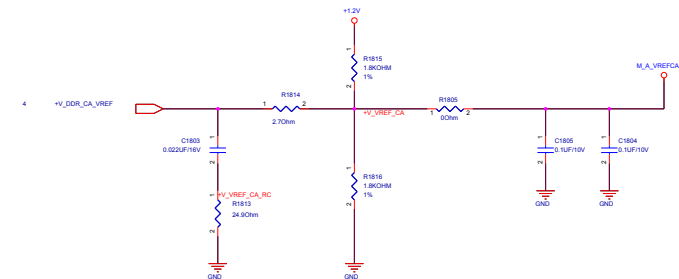


Figure 4-49. SKL U DDR4/-RS SODIMM V_{REF-CA} Overview



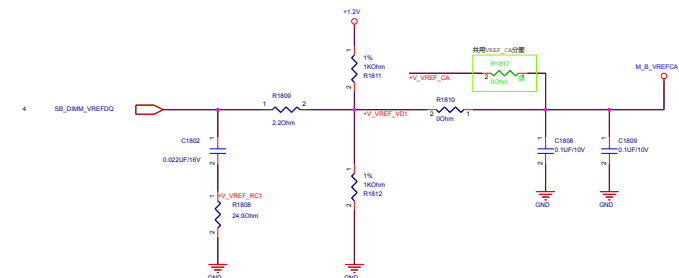
SO-DIMM0 Vref

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SO-DIMM1 Vref



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ASUS Q504UAK		1.0
Title : DIM_CA/DQ Voltage		
Dept: ASUS/TK COMPUTER	Engineer: EE	
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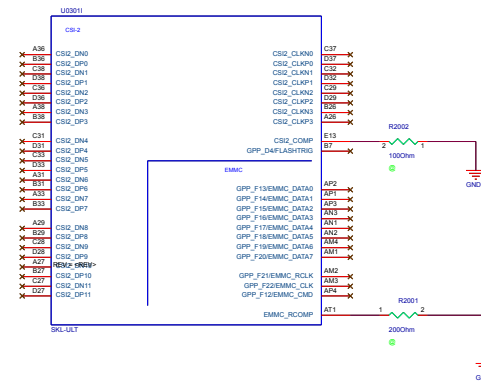
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ASUSTeK COMPUTER INC. NBS		Engineer: EE	
Size	Project Name	Rev	
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010 CPU POWER CAP

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012_***

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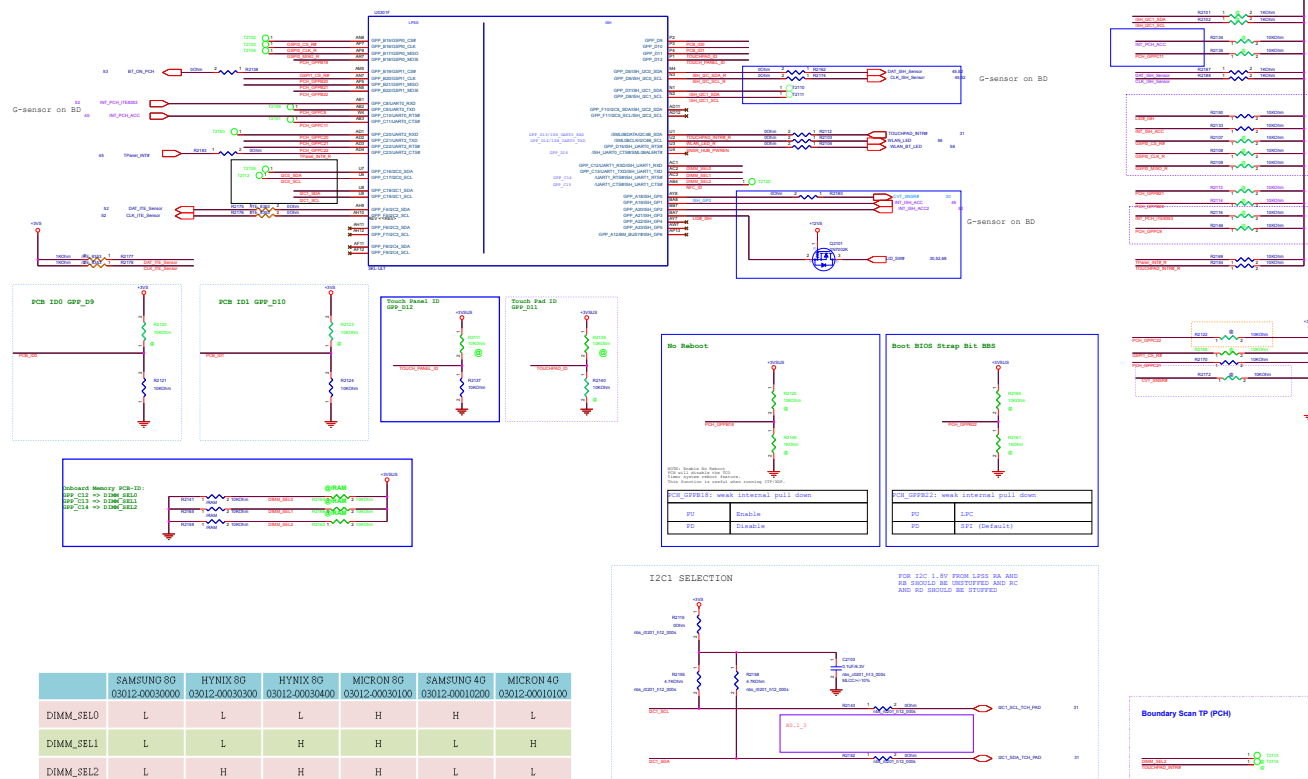
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SC022_CPU_PCH_AUDIO,SDIO,SD
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023 CPU PCH PCIE,USB,SATA

024 CPU PCH CLOCK SIGNAL



	SAMSUNG 8G 03012.00030000	HYNIX 8G 03012.00030300	HYNIX 8G 03012.00030400	MICRON 8G 03012.00030100	SAMSUNG 4G 03012.00010200	MICRON 4G 03012.00010100
DIMM_SEL0	L	L	L	H	L	L
DIMM_SEL1	L	L	H	H	L	H
DIMM_SEL2	L	H	H	H	L	L

PCN_GFPB18: weak internal pull down	
PU	Enable
PD	Disable

PCB_CSTR22: weak internal pull down	
PU	LPC
PD	SPI (Default)

ASUS		Title :	CPU_CFG_RSVD_GN
ASUS/M. COMPUTER INC. M8001		Engineer:	EE
Model	Product Name		
Q500U	Q500UAK		

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032_RESET CIRCUIT
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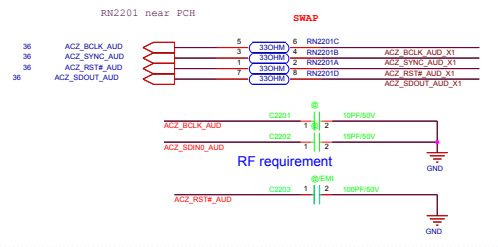
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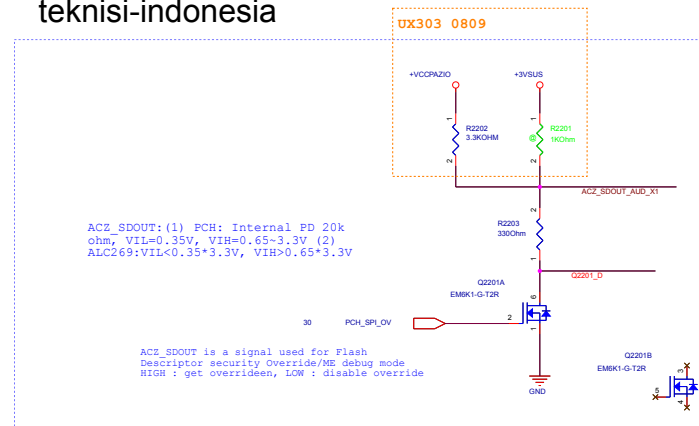
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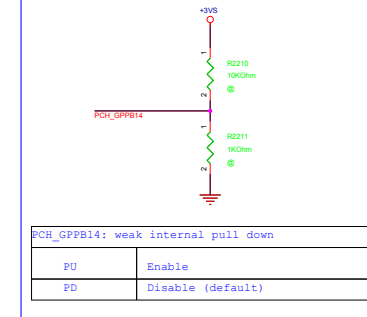
HD Audio



teknisi-indonesia



Top Swap Override



BOM

ASUS		Title : CPU_PCH_AUDIO,SDIO,SDXC	
ASUSTeK COMPUTER INC. NBS		Engineer: EE	
Doc	Project Name	Q504UAK	Rev: 1.0
Date: 10/20/2016, September 01, 2016		Drawn	By: AF

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SC022_CPU_PCH_AUDIO,SDIO,SD
XC

023 CPU PCH PCIE,USB,SATA

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S,RTC

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025 CPU PCH SYS POWER
```

026 CPU PCH POEWR,GND

027 CPU PCH POEWR,GND

028 PCH-SPI ROM,OTH

029 ****

030 KBC IT8995

031_EC_KB_TP_TPM

032_RESET CIRCUIT

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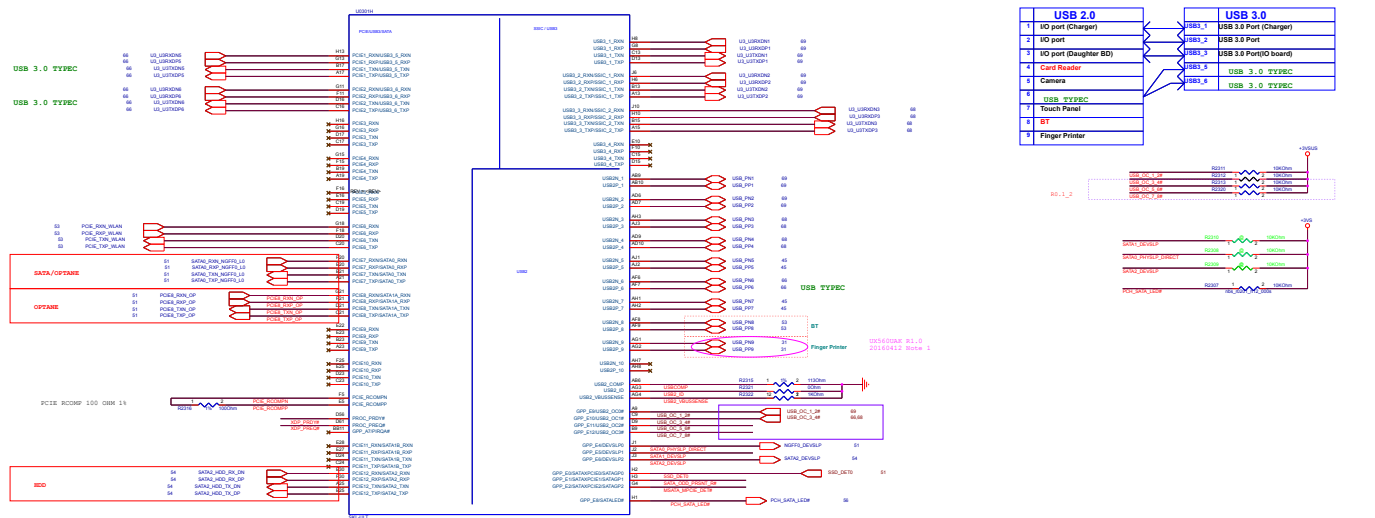
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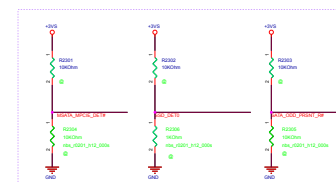
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PCI-E* X1	PCIe TRUNK DOWNSTREAM	Co-Lay	Clock
PCIE 0	N/A		
PCIE 1	N/A		
PCIE 2	N/A		
PCIE 3	N/A		
PCIE 4	N/A		
PCIE 5	N/A		Port1
PCIE 6	MLAG		
PCIE 7	100G UPI*8M		
PCIE 8	Optical		
PCIE 9	N/A		
PCIE 10	N/A		Port3
PCIE 11	N/A		
PCIE 12 (SFP28-10)	N/A		
	SD		

SATA PORT	SATA USAGE DEFAULT/OPTION
PORT 0	SSD
PORT 1	N/A <small>OPTION PORT</small>
PORT 2	HDD



TP for Boundary Scan Test

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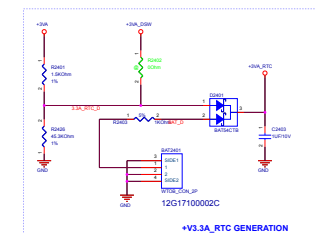
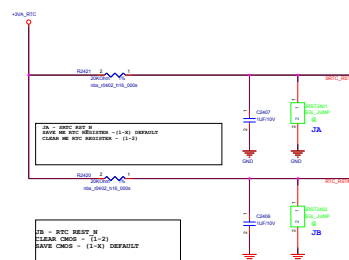
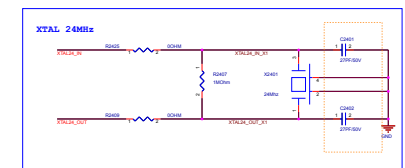
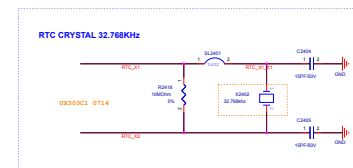
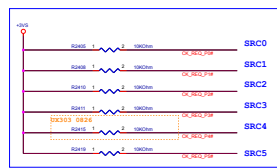
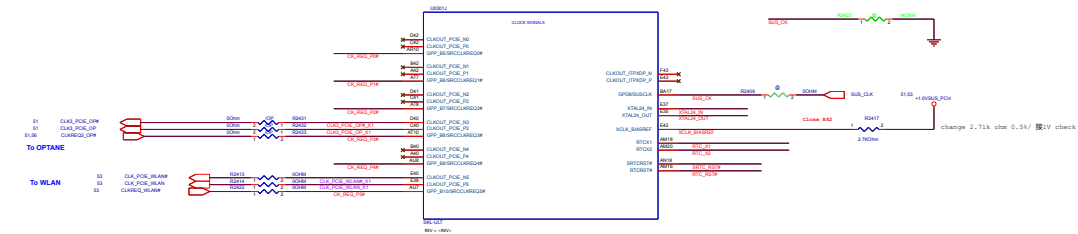
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032_RESET CIRCUIT
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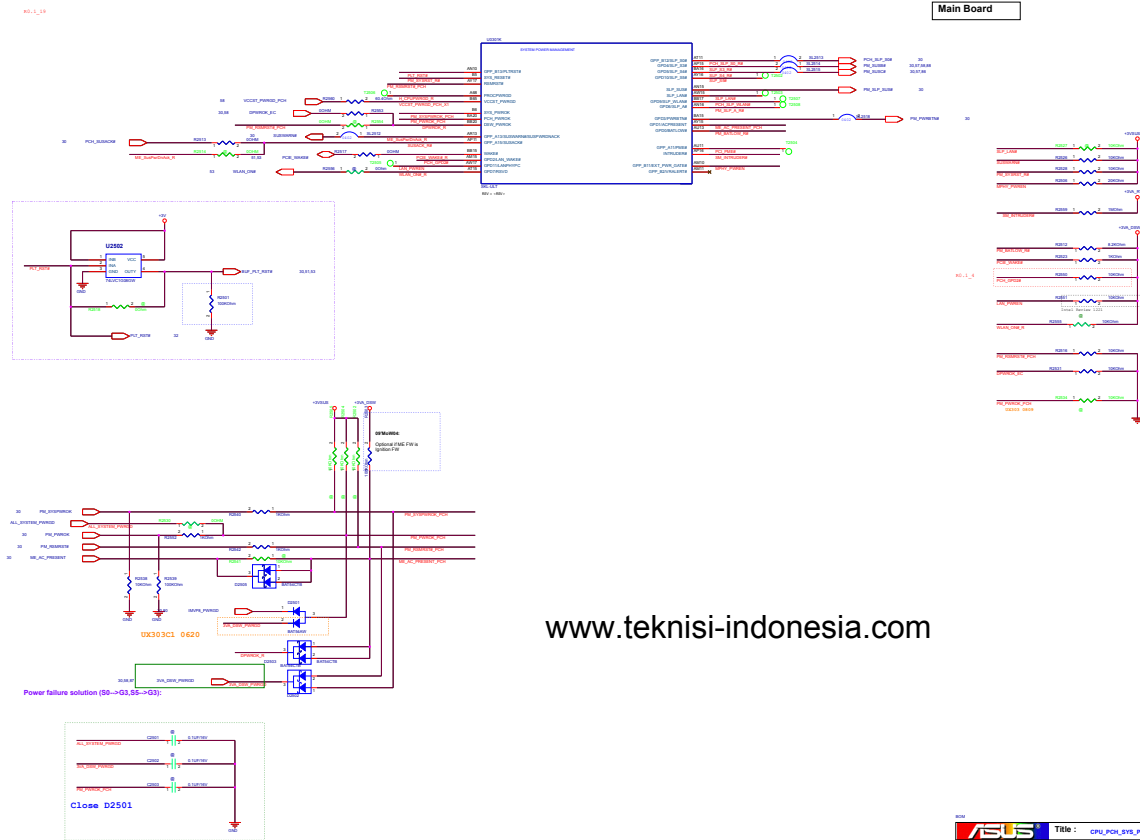
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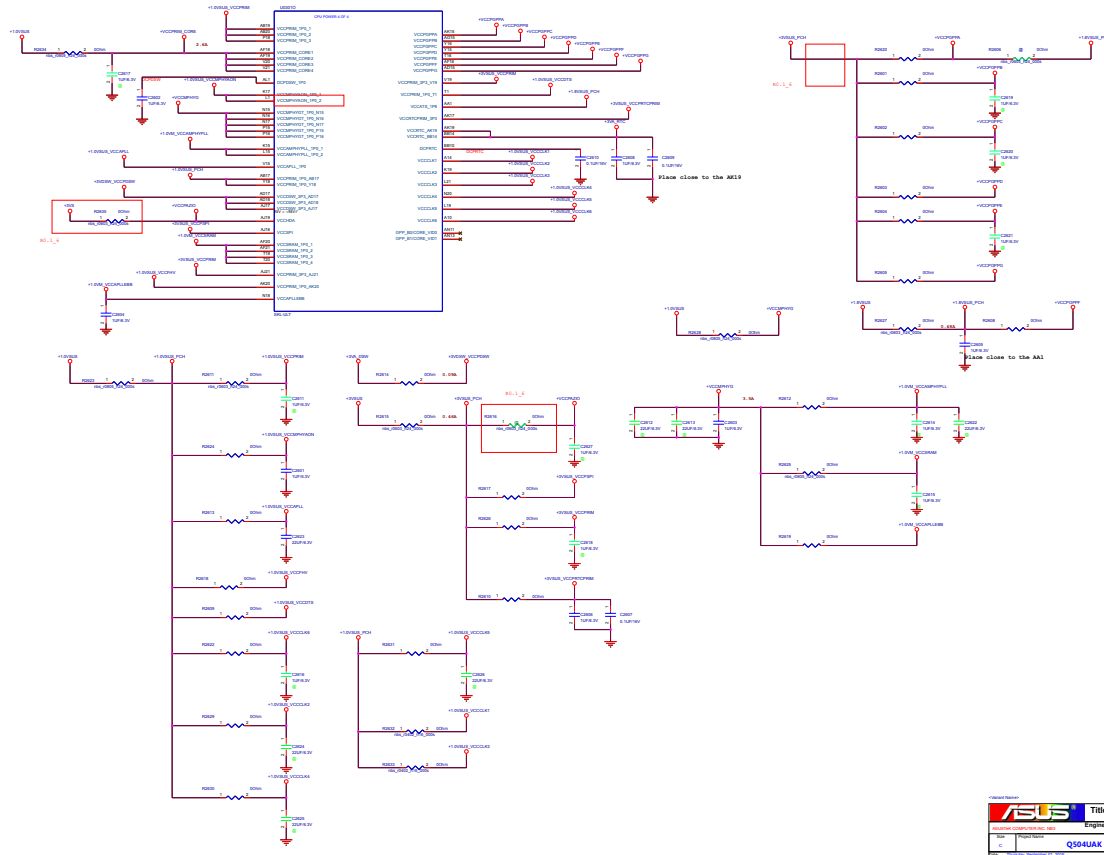
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031 EC KB TP TPM

032 RESET CIRCUIT

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034 ***

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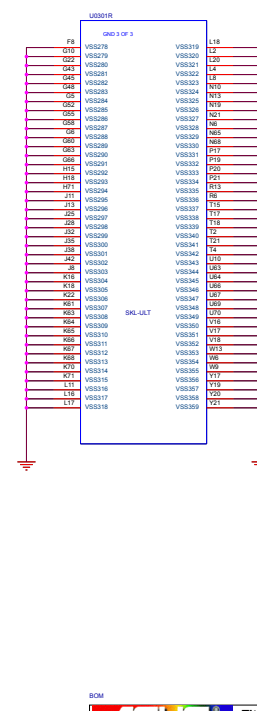
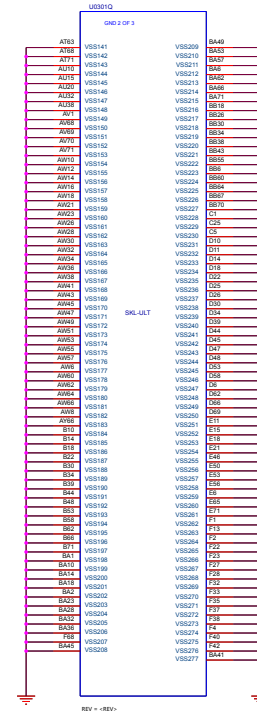
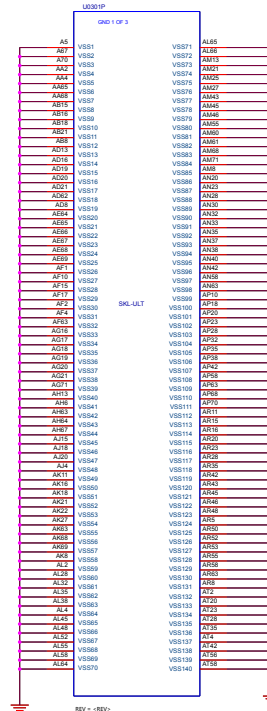
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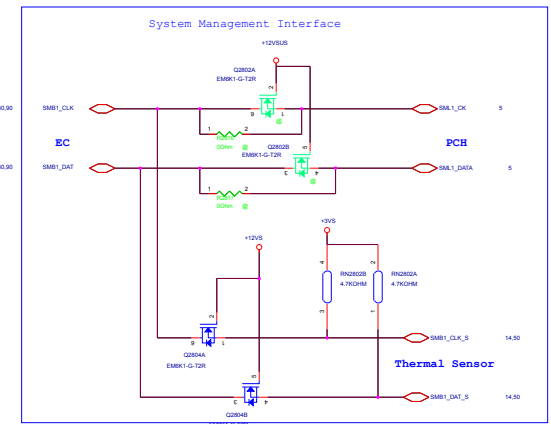
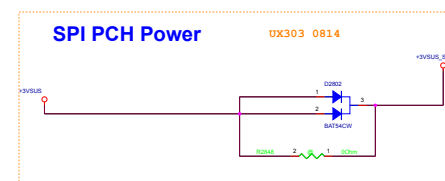
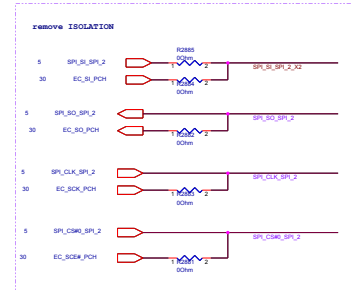
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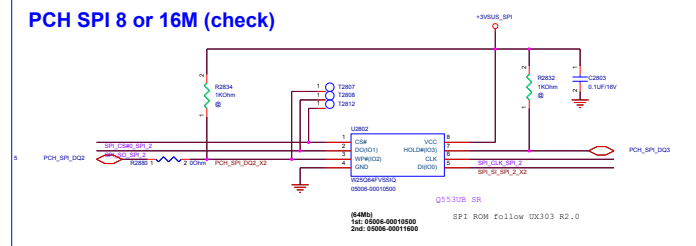
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PCH SPI 8 or 16M (check)



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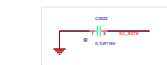
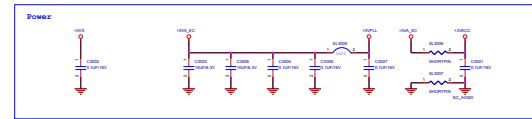
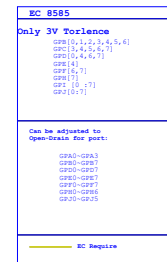
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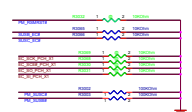
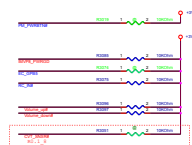
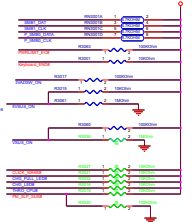
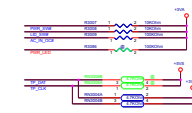
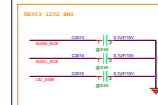
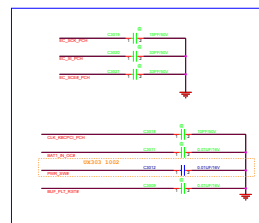
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17V Version	ADDS P/W
17999990/AS	02041-02430000



ASIS	Time : CR_KBC_FT821
17999990/AS	Engineer: EEE
Q5008 SWP_5	

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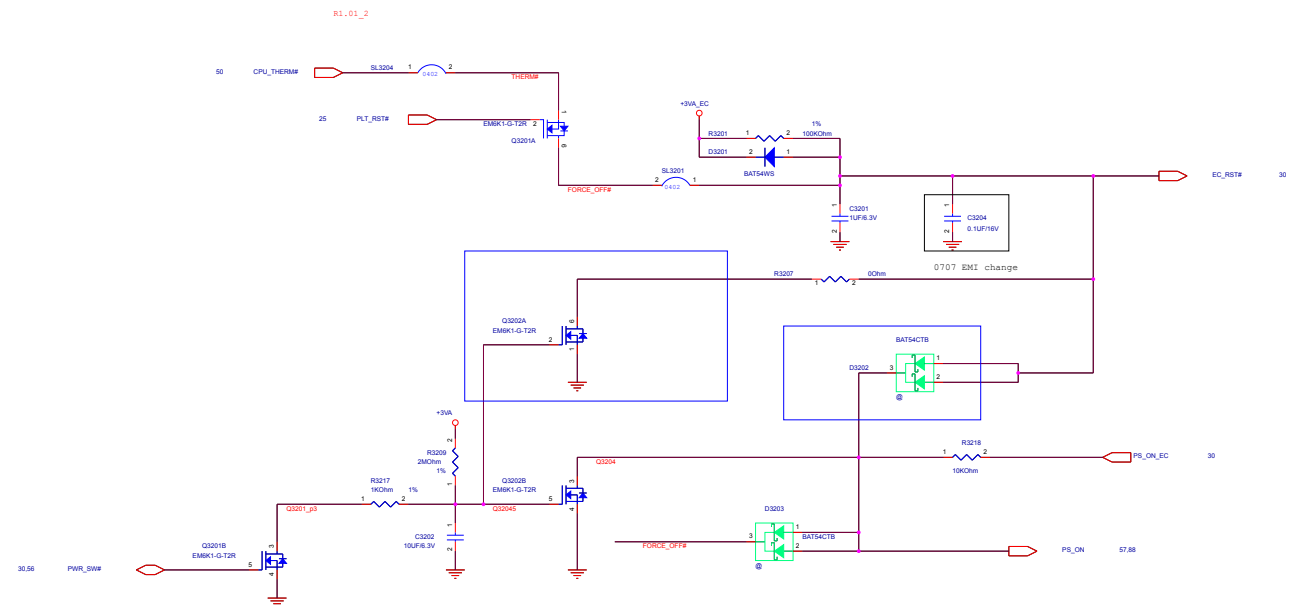
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Thermal Policy



Mount Q3202A or D3202 to reset EC

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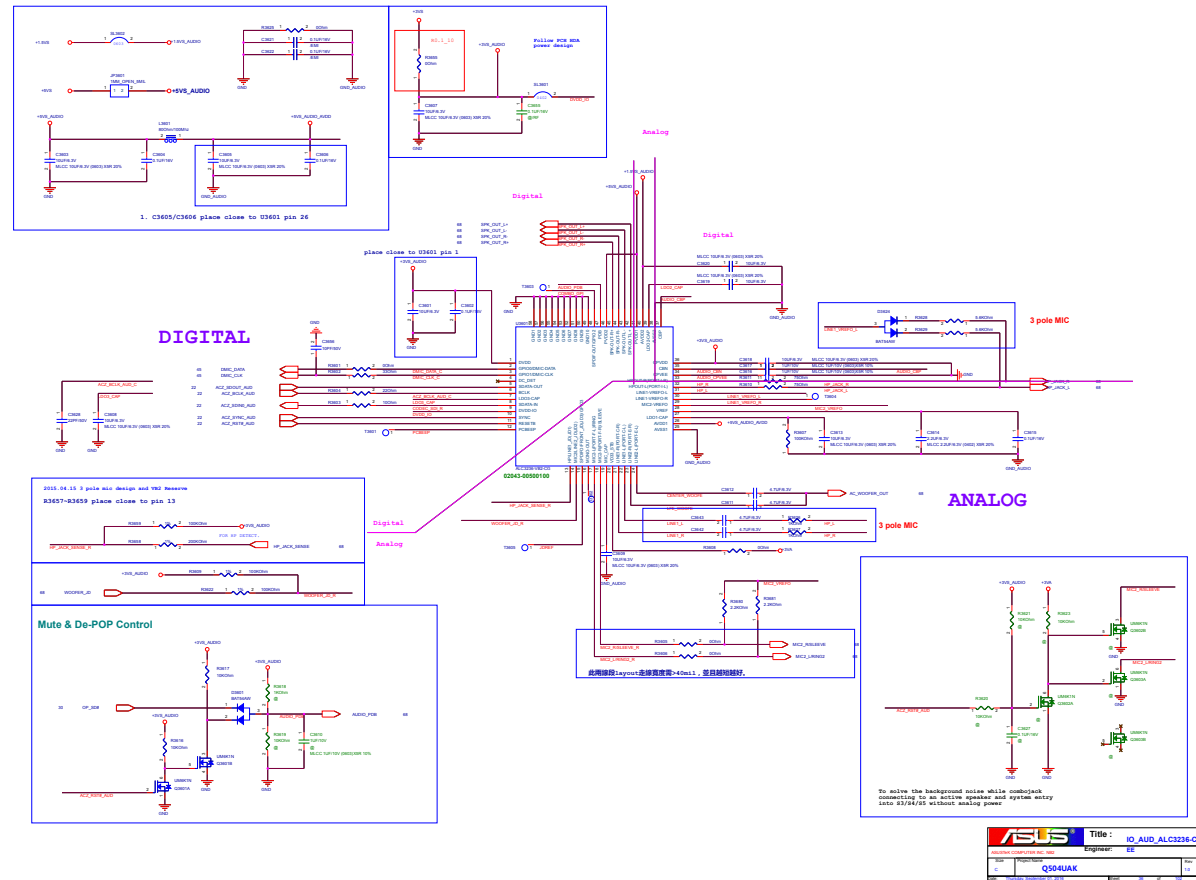
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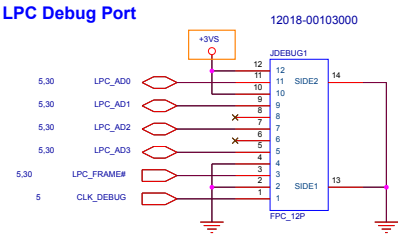
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Q553UB SR

+3V-->+3VS

防止S3睡不着的问题

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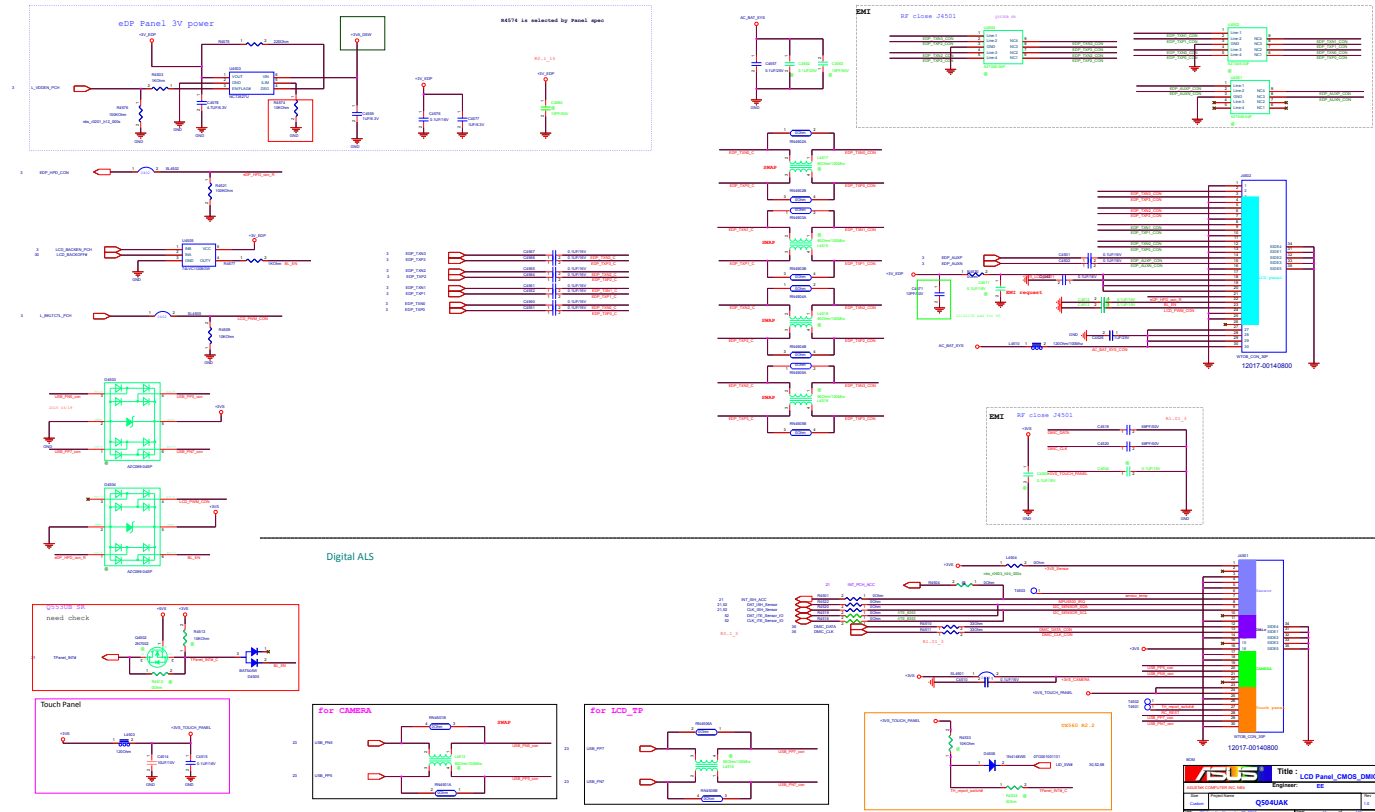
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058_PRO_Protect

059_EMI-ADD

060_PW_DC JACK / BAT CON

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065_ME_Conn & Skew Hole

066_USB 3.0 MB Type-C

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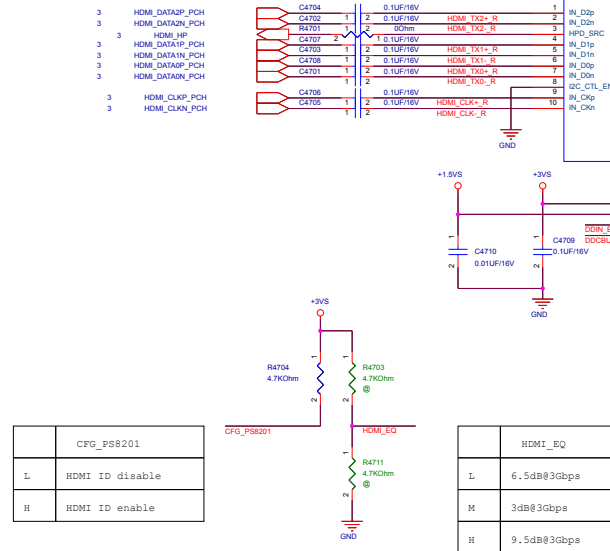
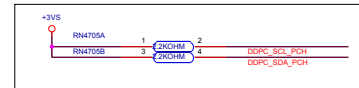
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HDMI Active-Level Shift



PS8201_PRE

	pre-emphasis
L	0 db
M	2.5 db
H	1.6 db

FIX can't read 4K2K HDMI Monitor EDID BUG
Reverse R4712, Mount R4713 Pull-down

*Variant Names

ASUS		Title : HDMI_Level_Shifter	
Adjuster component inc file		Engineer: EE	
Size	Project Name	Q504UAK	Rev
C			1.0
Date: Thursday, September 01, 2016		Page	47 of 108

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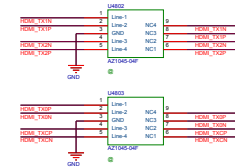
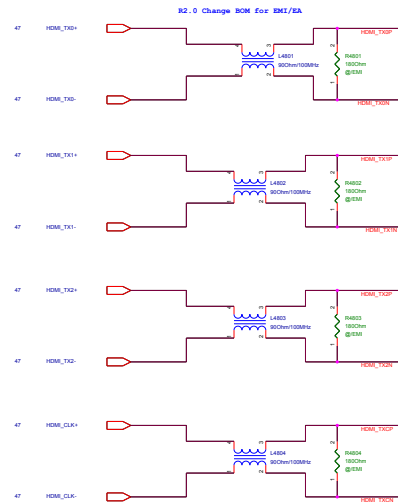
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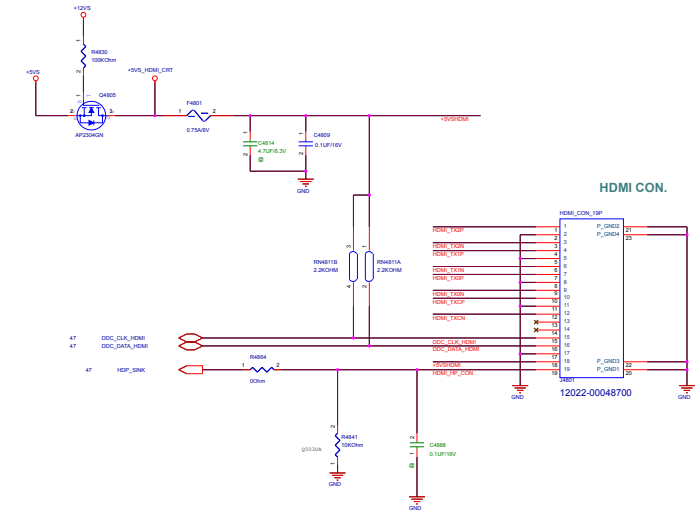
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Close to CONNECTOR

Near CON J4801



choke 1st: 09G092090330
2st: 09G092090109

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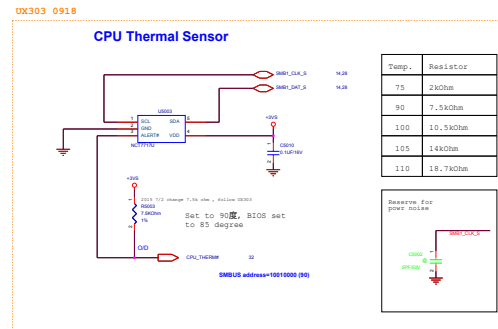
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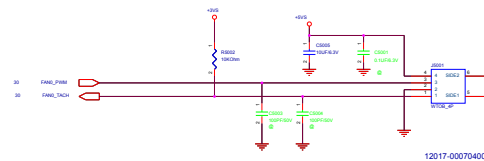
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CPU's FAN



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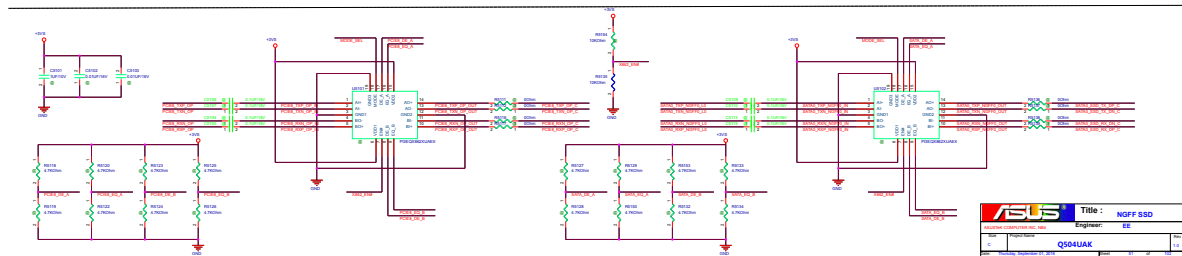
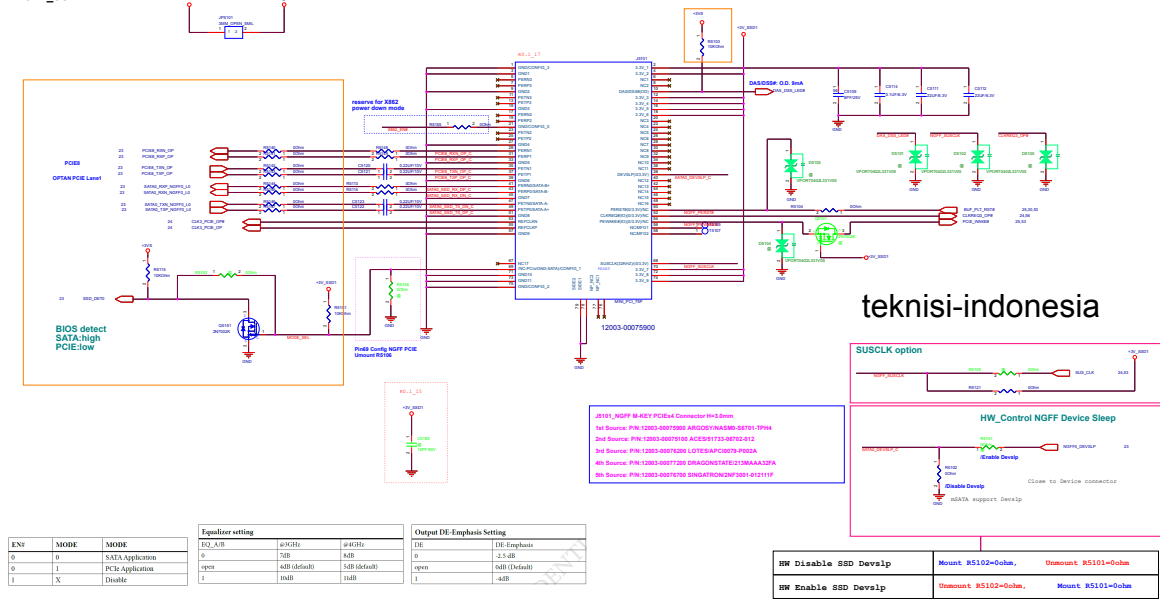
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NGFF_SSD



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047_HDMI Level Shift

048_HDMI con

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053_MiniCard_Wlan & BT

054_HDD

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056_LED_Indicator

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058_PRO_Protect

059_EMI-ADD

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065_ME_Conn & Skew Hole

066_USB 3.0 MB Type-C

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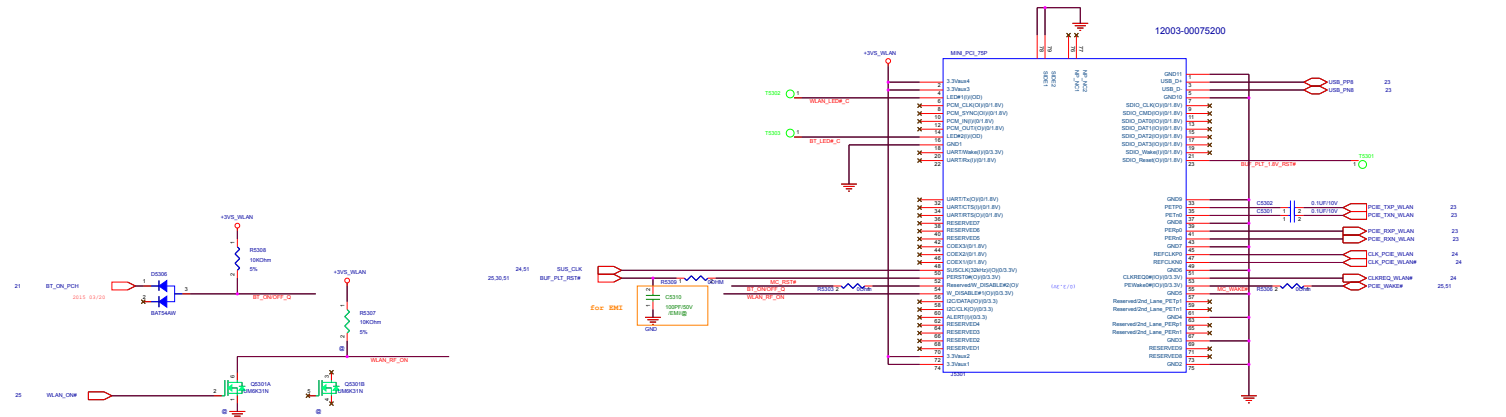
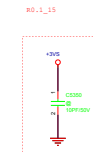
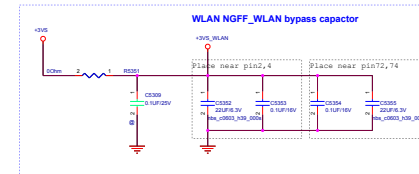
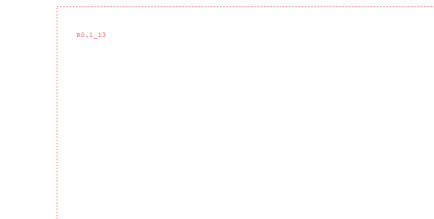
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BCM

ASUS		Title : MINICARD_Wlan & BT	
ASUSTeK COMPUTER INC. NME		Engineer: EE	
Doc	Project Name	Rev	1.0
C	Q504UAK		
Date: 2016/08/10 10:00:00		Drawn	EE

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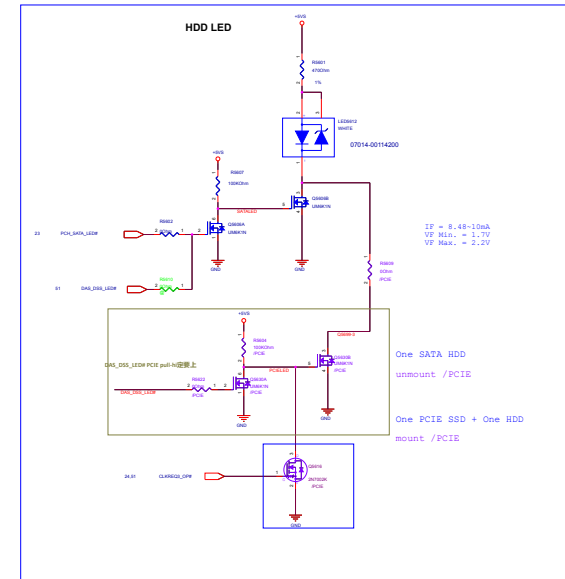
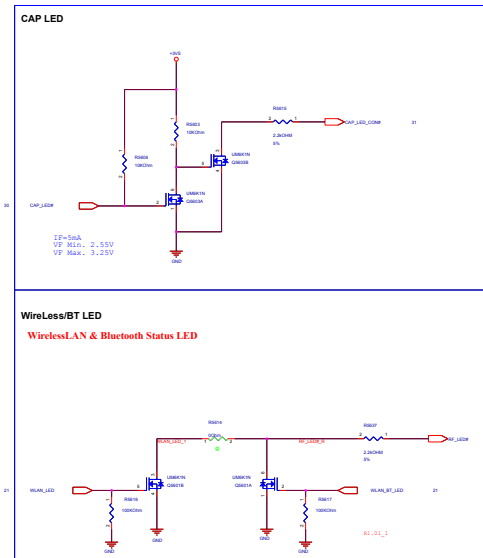
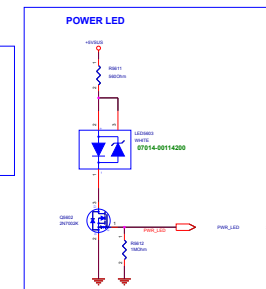
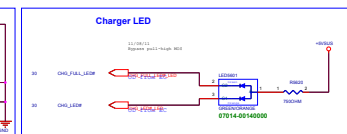
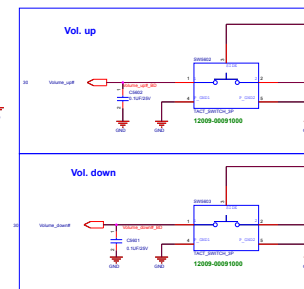
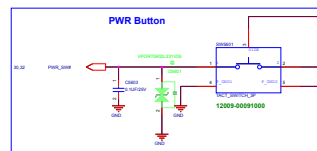
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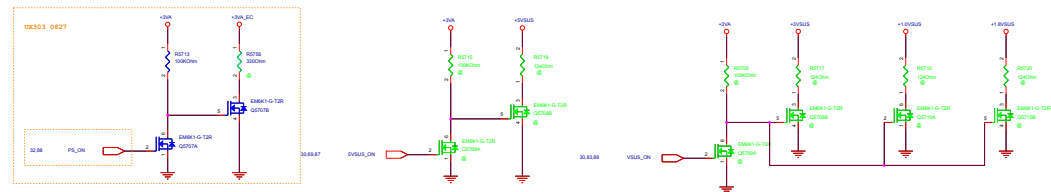
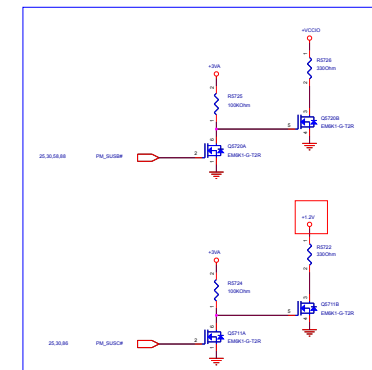
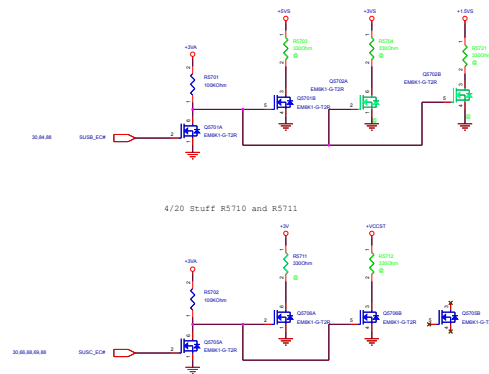
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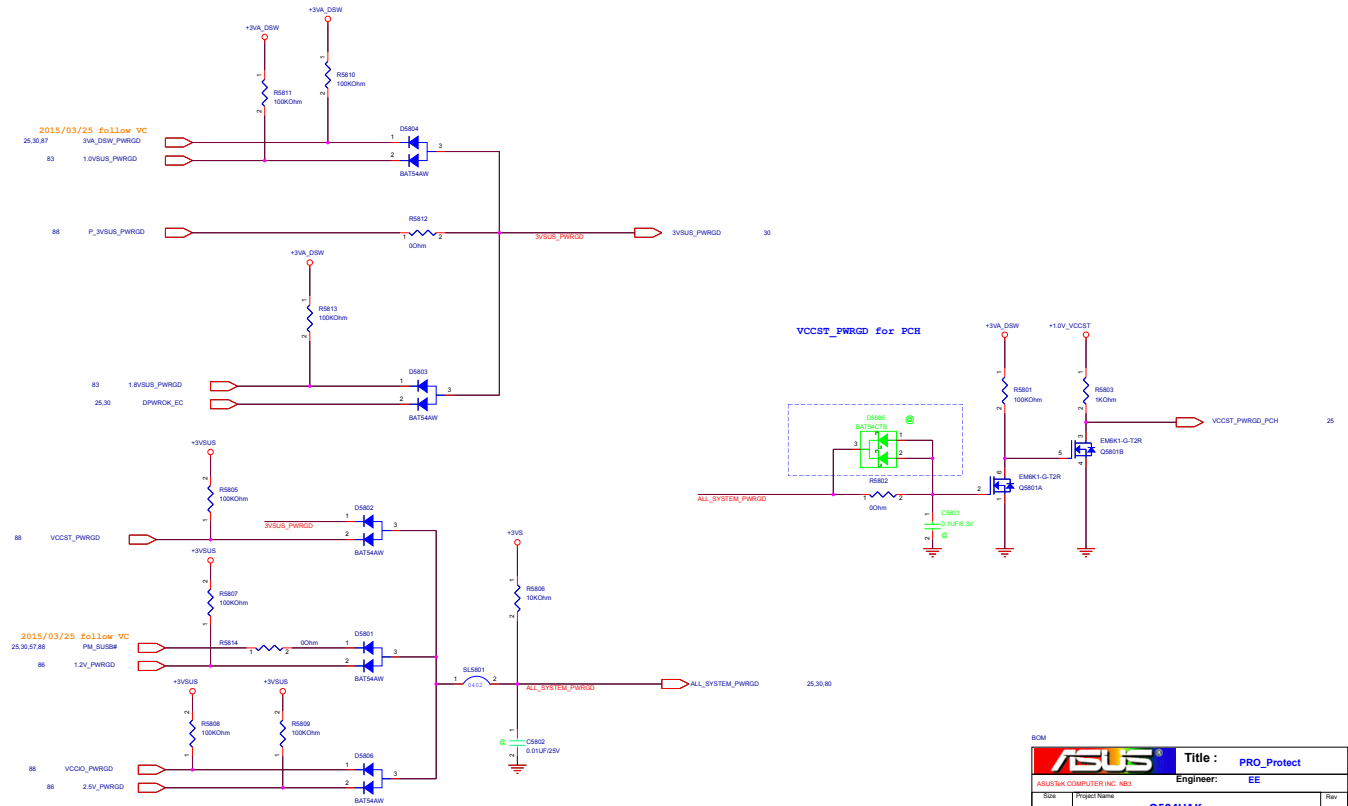
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074_VGA****

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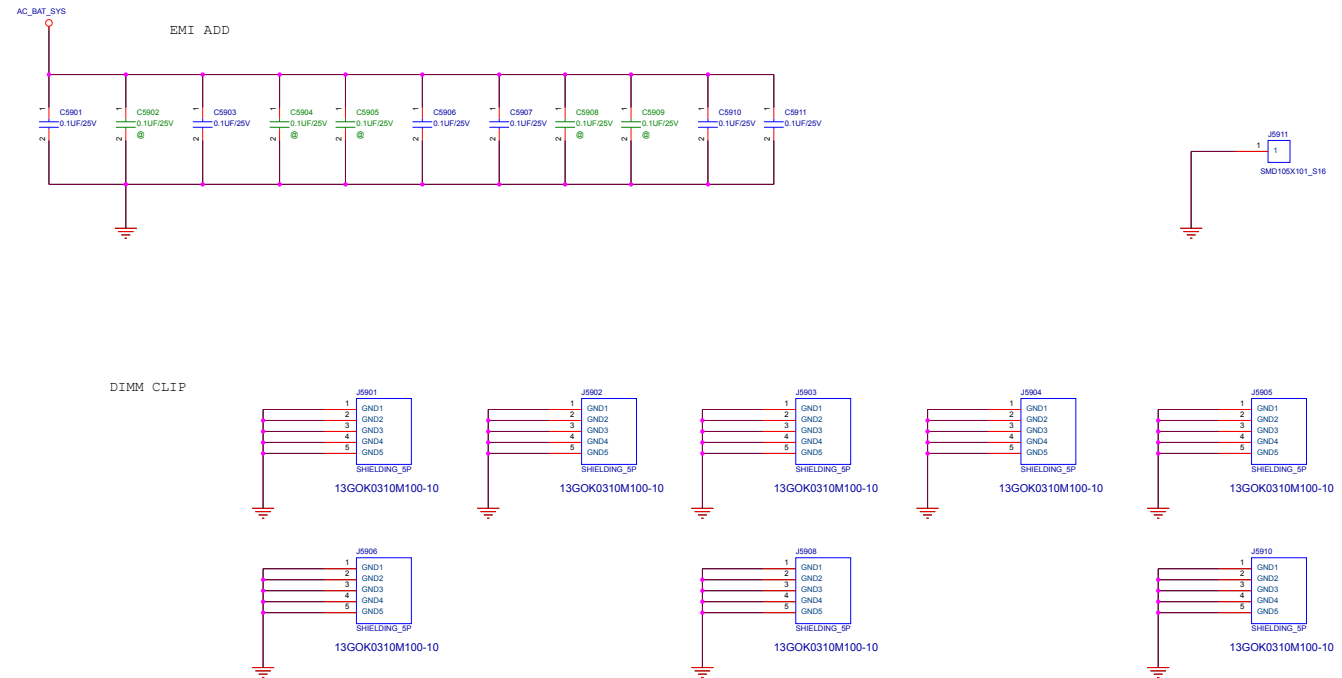
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Variant Name

ASUS		Title : NGFF_WWAN	
Asus New Computer Inc. - Hsinchu		Engineer: EE	
Size	Project Name	Rev	
C	Q504UAK	1.0	
Date: Thursday, September 01, 2016		Print	05 of 102

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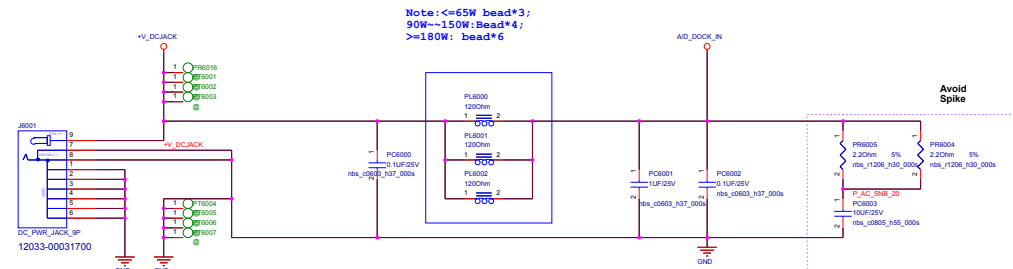
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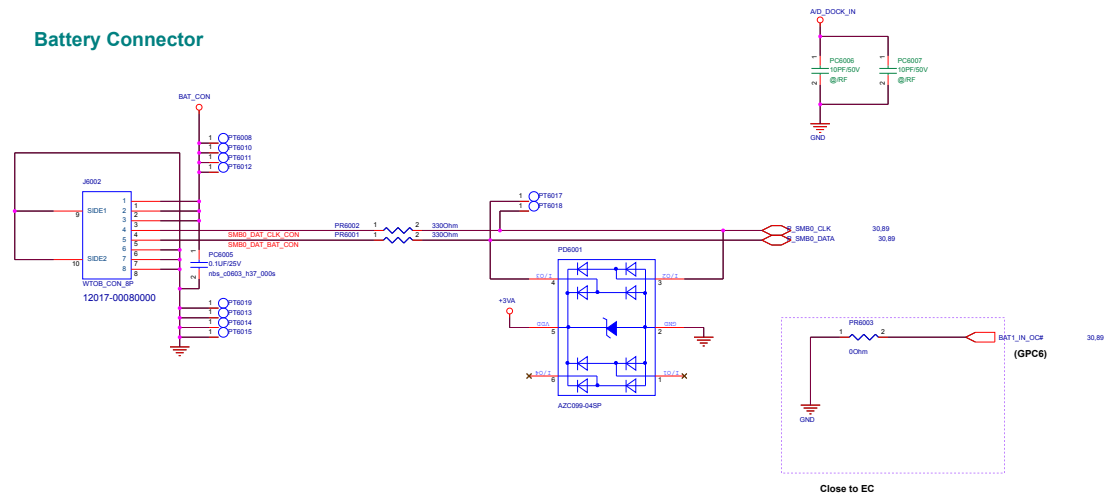
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Battery Connector



ASUS		Project Name	Rev
Q504UAK			0.1
Title : DC & BAT Connector			
Date	Dept.	Power Team	Engineer: Edison
Date: Thursday, September 01, 2016		Sheet	60 of 152

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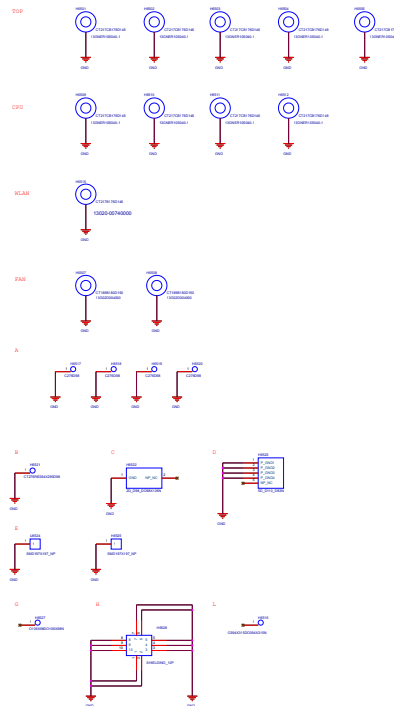
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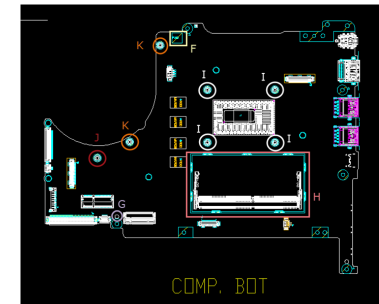
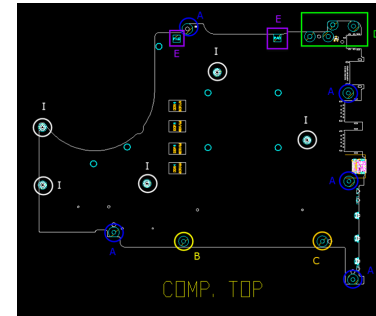
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編號	料號	數量
A	s09710	5
B	s09711	1
C	s09712	1
D	s09714	1
E	temp_TX15671_0004	2
F	temp_TX16523_0003	1
G	s09713	1
H	temp_T_0178	1
I	13GNER105040-1	9
J	13020-00740000	1
K	13G021061010	2

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ASUS		Title : ME_Conn & Skew Hole
ASUSTeK COMPUTER INC. TSM		Engineer: EE
Doc	Project Name	Q504UAK
Date	Thursday, September 01, 2016	Sheet 60 of 100

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073_VGA****
074_VGA****

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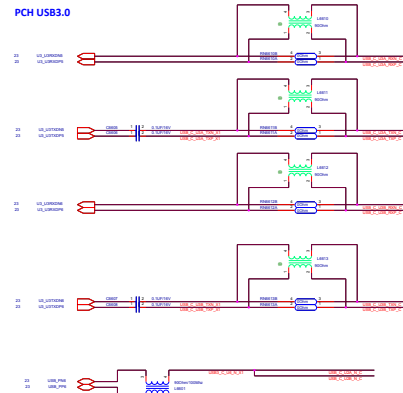
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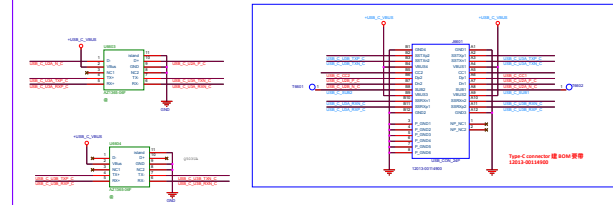
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PCH USB3.0

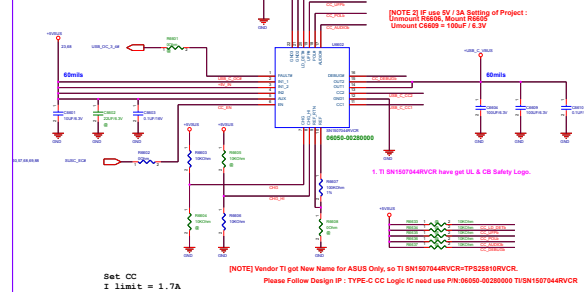


Type C Con. & USB3.0 EMI protect



TI CC Logic SN1507044RVCR = TPS25810RVCR, DFP (Host), 5V / 1.5A

(ASUS Only Use)

Set CC
1.1mA = 1.7A

[NOTE] Vendor TI got New Name for ASUS Only, so TI SN1507044RVCR=TPS25810RVCR.
Please Follow Design IP: TYPE-C CC Logic IC need use PIN:06050-00200000 TISN1507044RVCR

Table 3. USB Type-C Current Advertisement

CHG	CHG_HI	CC CAPABILITY BROADCAST	CURRENT LIMIT (typ)	LOAD DETECT THRESHOLD (typ)
0	0	STD	1.7 A	NA
0	1	STD	1.7 A	NA
1	0	1.5 A	1.7 A	NA
1	1	3 A	3.4 A	1.95 A

Client Name:

ASUS		Project Name	Rev
Q504UAK			1.0
Title : USB 3.0 MB Type-C			
Drawn	Dept.	ASUS COMPUTER INC.	Engineer: EE
Date: Thursday, September 01, 2016	Sheet	46	of 102

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- 056_LED_Indicator
- 057_DSG_Discharge
- 058_PRO_Protect
- 059_EMI-ADD
- 060_PW_DC JACK / BAT CON
- 061_
- 062_
- 063_
- 064_
- 065_ME_Conn & Skew Hole
- 066_USB 3.0 MB Type-C
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- 068_MB to IO connector
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- 070_VGA****
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- 072_VGA****
- 073_VGA****
- 074_VGA****

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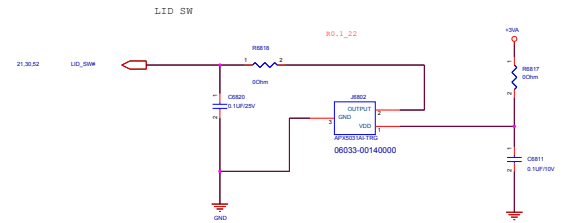
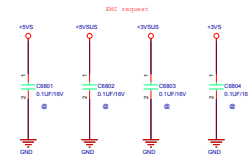
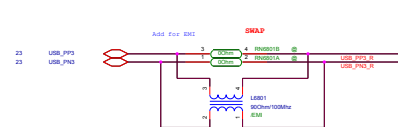
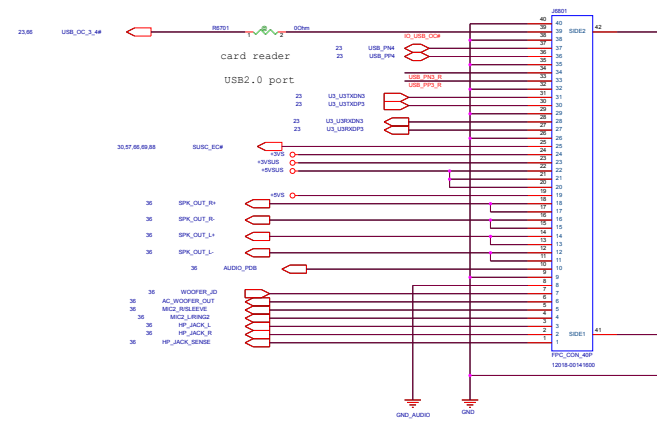
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 066_USB 3.0 MB Type-C
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 071_VGA****
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 073_VGA****
 074_VGA****

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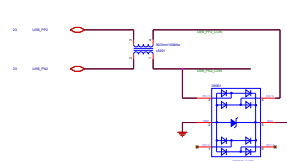
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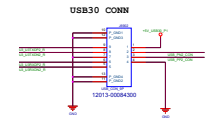
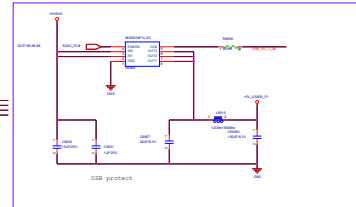
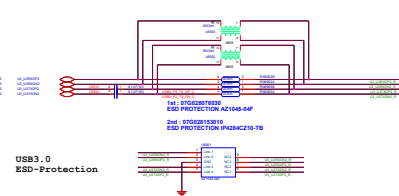
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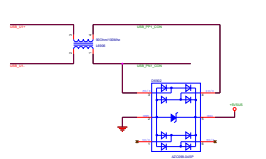
USB3.0_Port2
 USB2.0 EMI-Protection &
 ESD-Protection



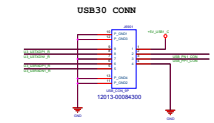
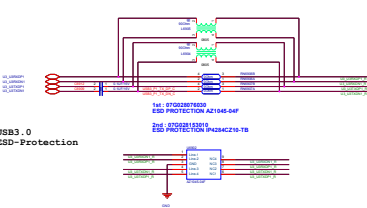
USB3.0
 EMI-Protection



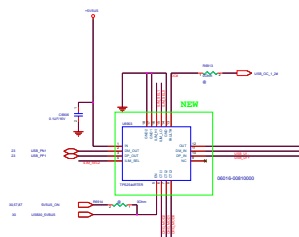
USB3.0_Port1 (Charger)
 USB2.0 EMI-Protection &
 ESD-Protection



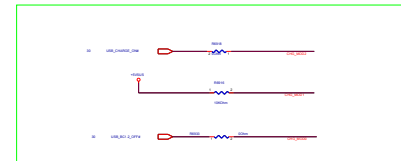
USB3.0
 EMI-Protection



USB3.0_Port1



USB Charge Circuit (For PORT 1)



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083_PW_+1.0VSUS/+1.8VSUS (6001)

084_PW_+1.5VS (UP0107)

085 PW ***

086_PW_+1.2V/+VTT/+2.5V (901
1)

087 PW +3VADSW/+5VSUS

088 PW LOAD SWITCH

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R)

090 PW PROTECTION

091_PW_

092 PW ***

093 PW ***

094 PW ***

095 PW ***

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007 DW

008 DW/ ***

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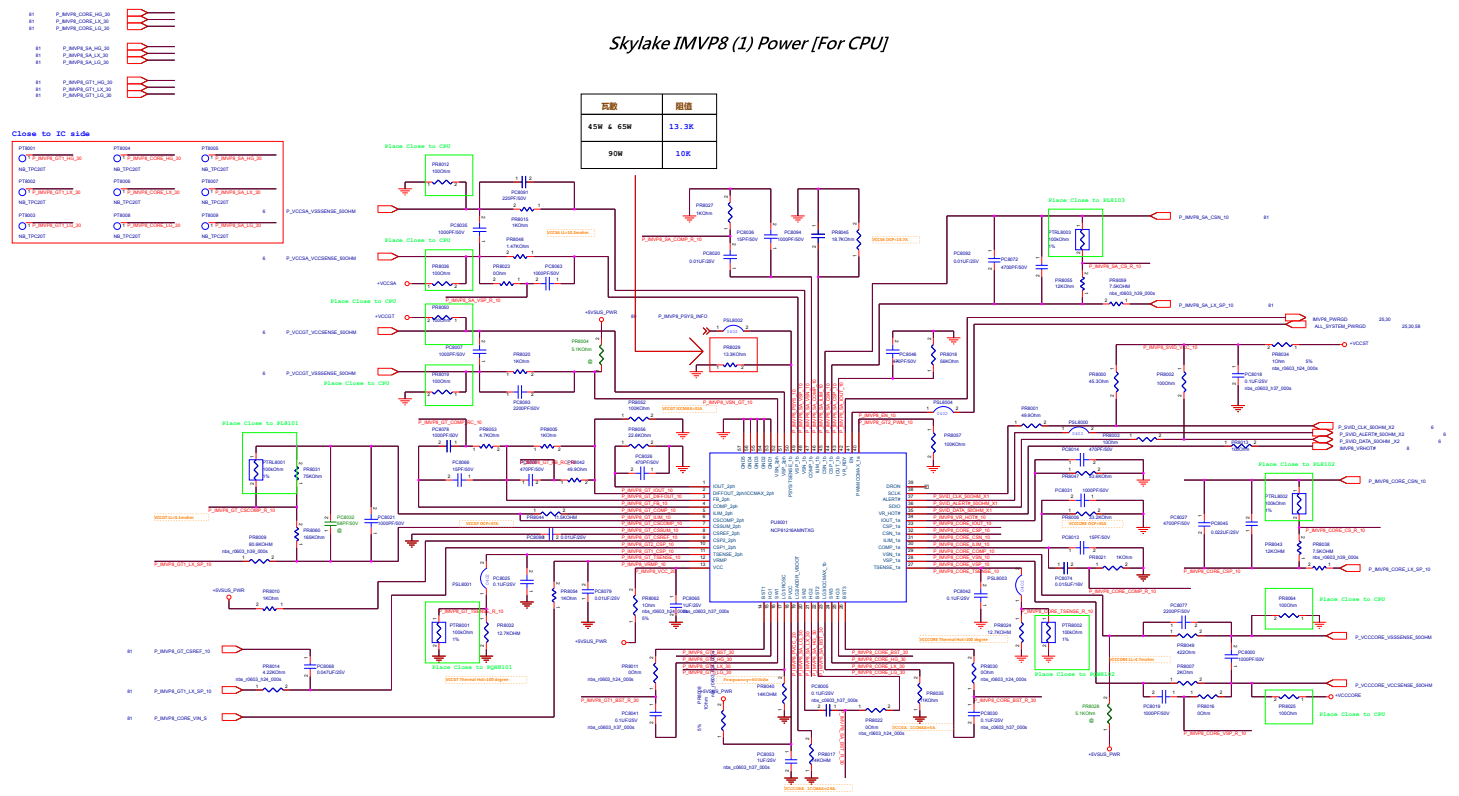
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087_PW_+3VADSW/+5VSUS

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089_PW_CHARGER(SN2867RUY
R)

090_PW_PROTECTION

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092_PW_***

093_PW_***

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095_PW_***

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097_PW_

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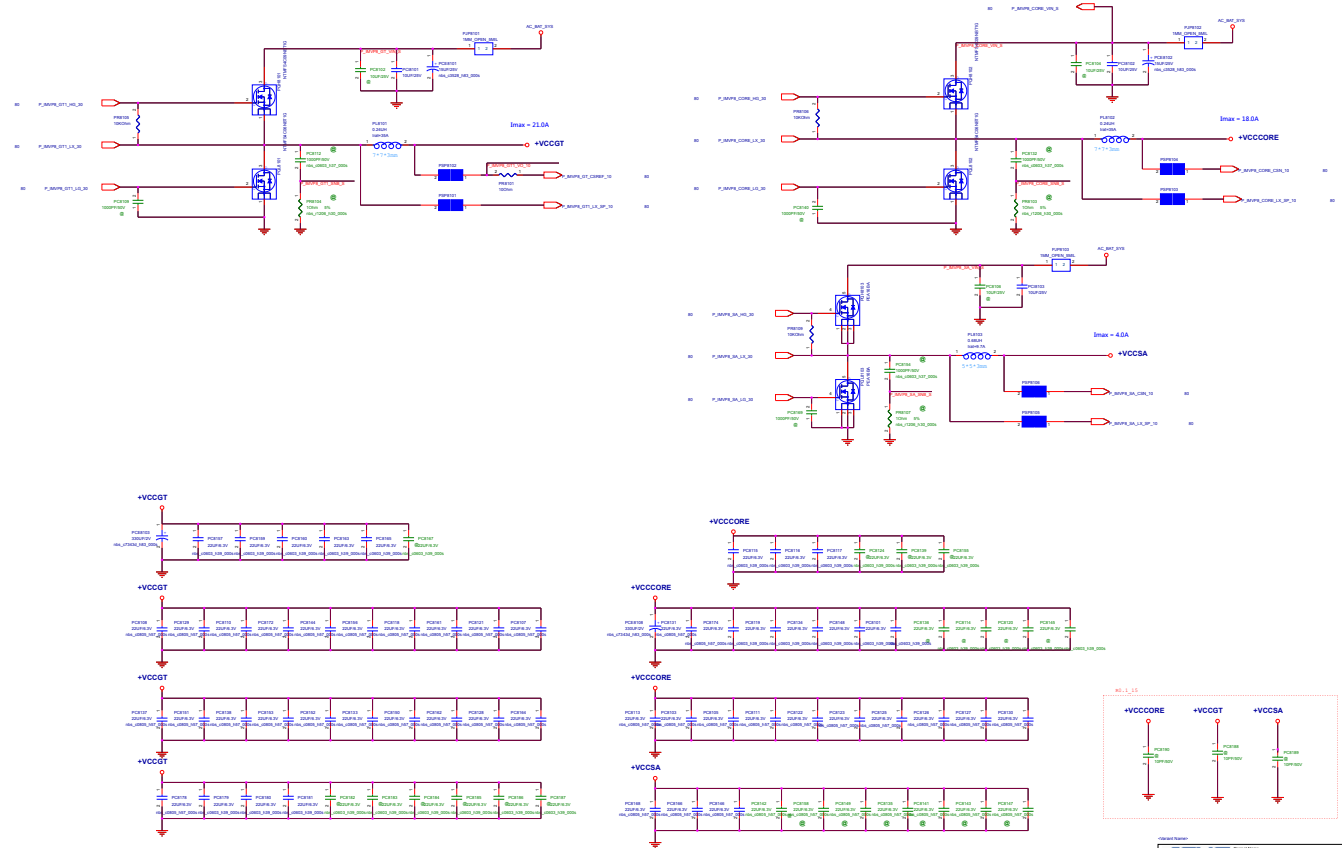
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Skylake IMVP8 Power (2) [For CPU]



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084_PW_+1.5VS (UP0107)

085_PW_***

086_PW_+1.2V/+VTT/+2.5V (901
1)

087_PW_+3VADSW/+5VSUS

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089_PW_CHARGER(SN2867RUY
R)

090_PW_PROTECTION

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092_PW_***

093_PW_***

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095_PW_***

096_PW_***

097_PW_

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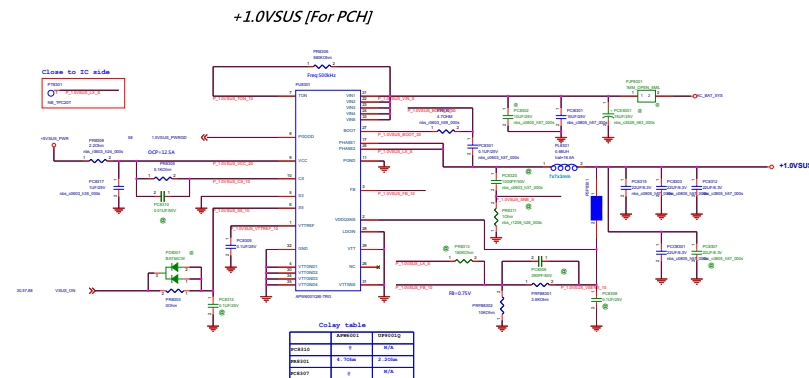
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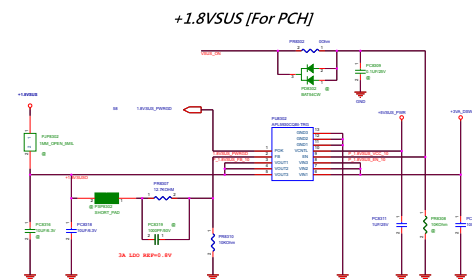
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081_PW_SKYLAKE-U (2)
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083_PW_+1.0VSUS/+1.8VSUS (6001)
084_PW_+1.5VS (UP0107)
085_PW_***
086_PW_+1.2V/+VTT/+2.5V (9011)
087_PW_+3VADSW/+5VSUS
088_PW_LOAD SWITCH
089_PW_CHARGER(SN2867RUYR)
090_PW_PROTECTION
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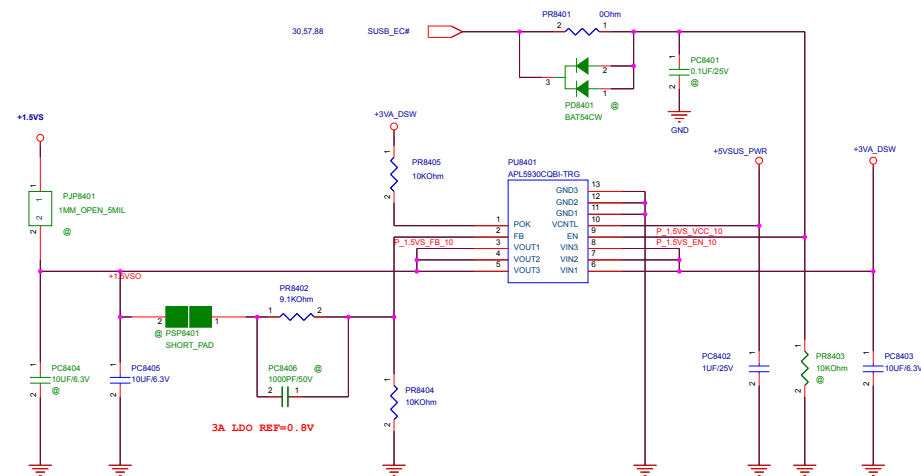
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+1.5VS [For Audio]

<Variant Name>

ASUS		Project Name	Rev
		Q504UAK	1.0
Title : +1.5VS			
Size	Custom	Dept.: ASUS/TEK COMPUTER INC.	Engineer: Doremi
Date: Thursday, September 01, 2016		Sheet	84 of 102

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081_PW_SKYLAKE-U (2)
082_PW_***
083_PW_+1.0VSUS/+1.8VSUS (6001)
084_PW_+1.5VS (UP0107)
085_PW_***
086_PW_+1.2V/+VTT/+2.5V (9011)
087_PW_+3VADSW/+5VSUS
088_PW_LOAD SWITCH
089_PW_CHARGER(SN2867RUYR)
090_PW_PROTECTION
091_PW_
092_PW_***
093_PW_***
094_PW_***
095_PW_***
096_PW_***
097_PW_
098_PW_***

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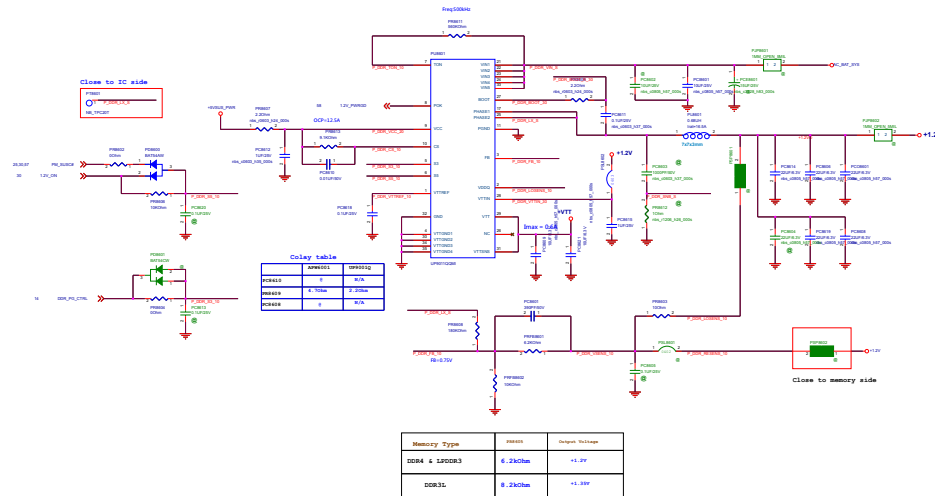
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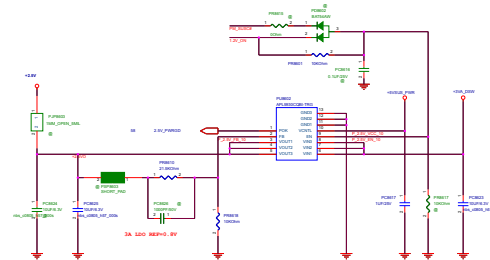
+1.2V / +VTT / +2.5V[For Memory]



If DDR3L is used, this LDO will not be used.

Net name is needed to change if LPDDR3 is used.

Memory Type	Power	Output Voltage
DDR4	1.2V	+1.2V
LPDDR3	1.5V	+1.5V



Schematic Page List

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076_VGA****

077_VGA****

078_VGA***

079_****

080_PW_SKYLAKE-U (1)

081_PW_SKYLAKE-U (2)

082_PW_***

083_PW_+1.0VSUS/+1.8VSUS (6001)

084_PW_+1.5VS (UP0107)

085_PW_***

086_PW_+1.2V/+VTT/+2.5V (9011)

087_PW_+3VADSW/+5VSUS

088_PW_LOAD SWITCH

089_PW_CHARGER(SN2867RUYR)

090_PW_PROTECTION

091_PW_

092_PW_***

093_PW_***

094_PW_***

095_PW_***

096_PW_***

097_PW_

098_PW_***

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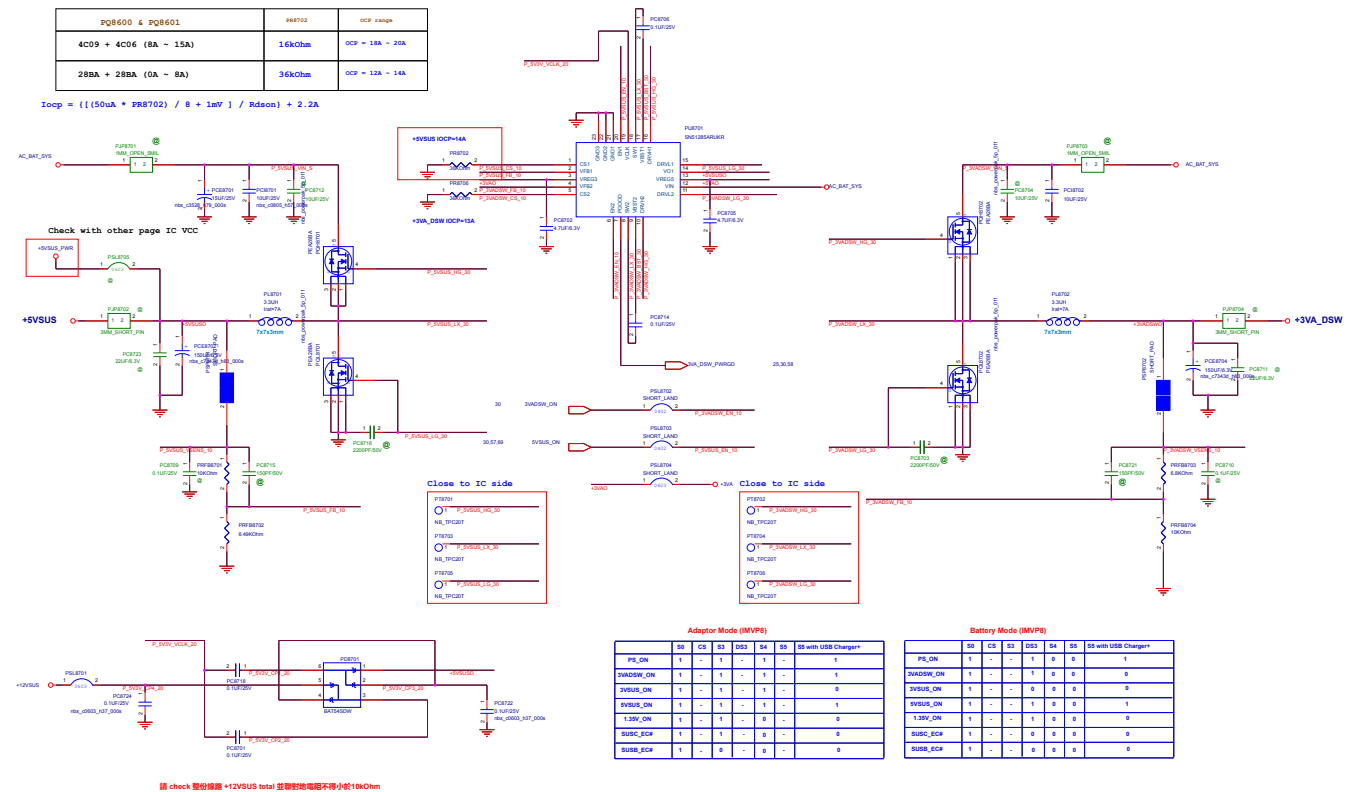
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+3VA_DSW / +5VSUS [System Power]



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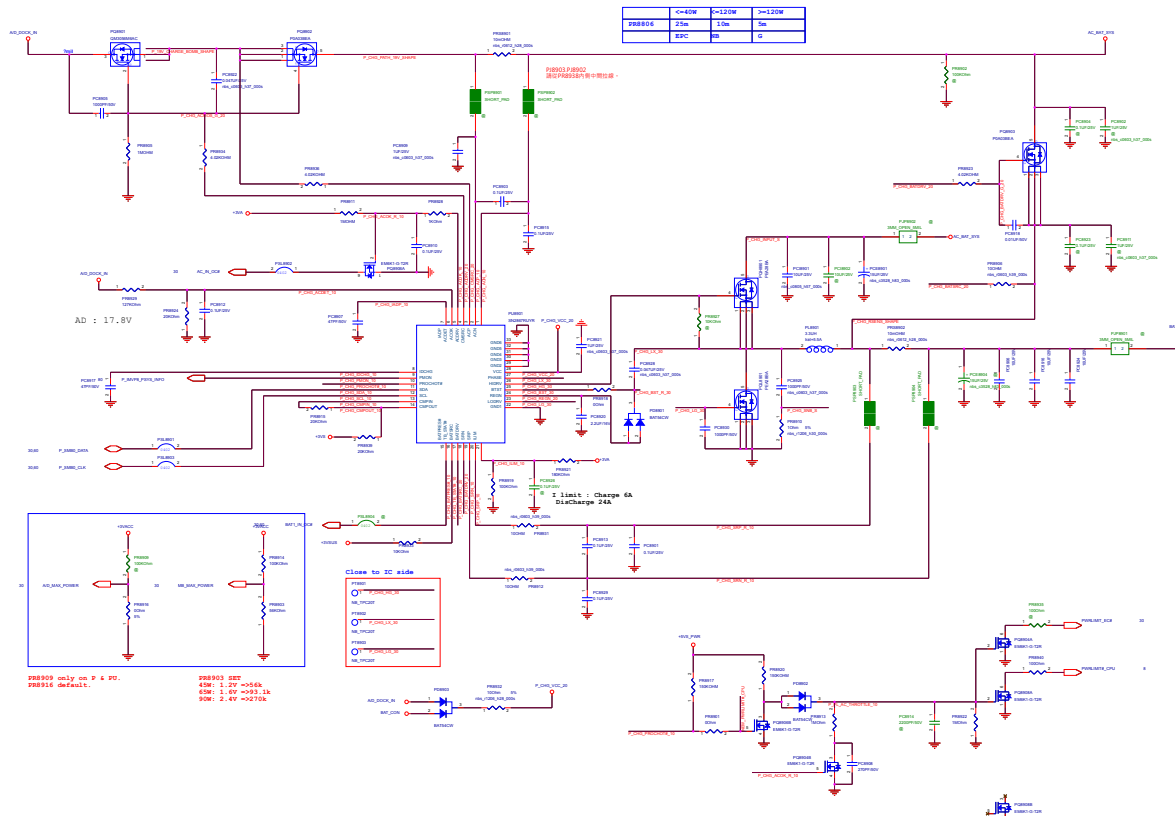
081

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PR8909 only on P & FU.
PR8916 default.

```

PR8903 SET
45W: 1.2V =>56k
65W: 1.6V =>93.1k
90W: 2.4V =>270k

```

Close to 1

• 5/18/21

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FORMS

ASUS		Project Name	Rev
Title: FW_CHARGER		Q504UAK	0.1
Size	Dept.: NIS Power Dept	Engineer: Rock	
AJ			
Date: Thursday, September 01, 2016	Sheet: 49	of	102

- 076_VGA****
- 077_VGA****
- 078_VGA***
- 079_****
- 080_PW_SKYLAKE-U (1)
- 081_PW_SKYLAKE-U (2)
- 082_PW_***
- 083_PW_+1.0VSUS/+1.8VSUS (6 001)
- 084_PW_+1.5VS (UP0107)
- 085_PW_***
- 086_PW_+1.2V/+VTT/+2.5V (901 1)
- 087_PW_+3VADSW/+5VSUS
- 088_PW_LOAD SWITCH
- 089_PW_CHARGER(SN2867RUY R)
- 090_PW_PROTECTION
- 091_PW_
- 092_PW_***
- 093_PW_***
- 094_PW_***
- 095_PW_***
- 096_PW_***
- 097_PW_
- 098_PW_***

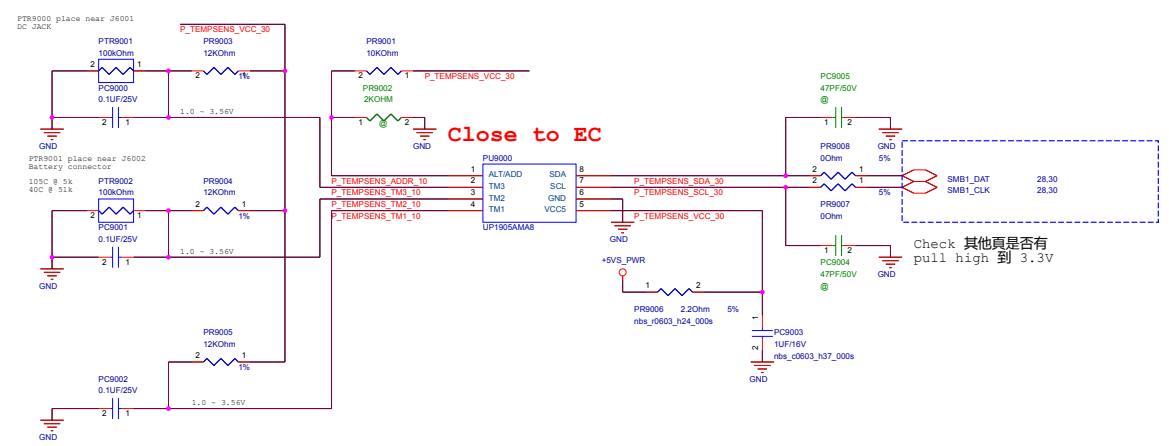
Address Selection Table

Address	0x7E	0x7C	0x7A	0x78	0x76	0x74	0x72	0x70
PR9001	10k	1.5k	2k	3.6k	3.9k	4.3k	5.1k	6k
PR9002	Open	8.2k	6.2k	6.8k	4.7k	3.6k	2.7k	2k

Register Address

Address	0x00	0x01	0x02	0x03	0x04	0x05	0x06
R/W	W	W	W	R	R	R	R
Function	Temp. alert threshold setting			Sensed temp. data		bit 4 = 0 bit 5 = 0 bit 6 = 0 When ALERT# assert	

ALERT# pull low if sensed temp. is higher than setting



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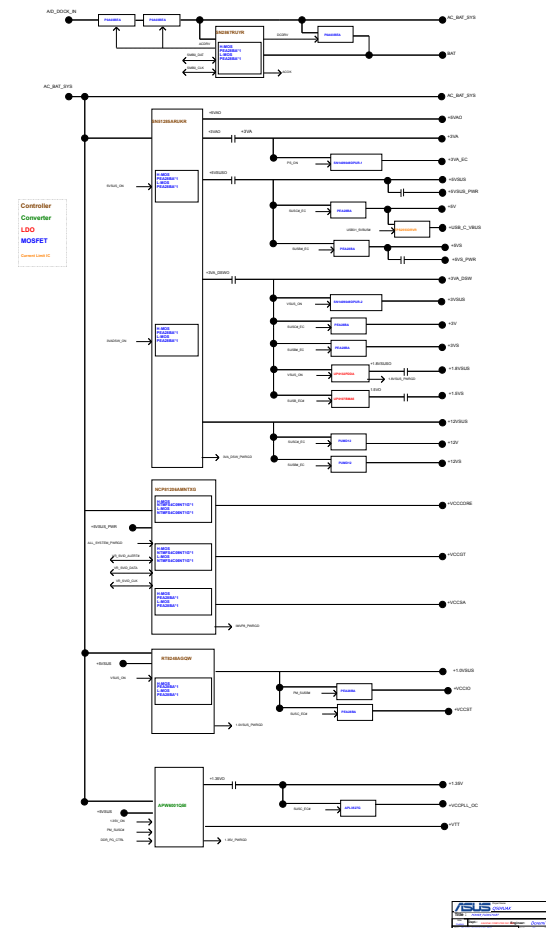
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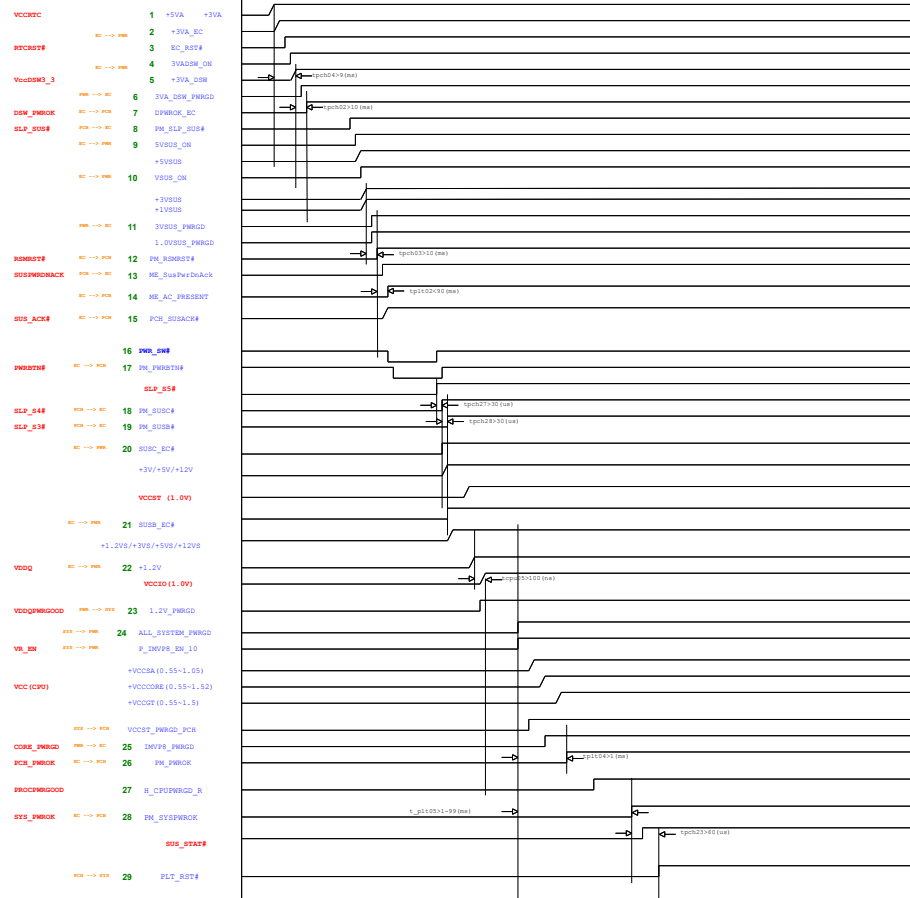


Hide

009 DW ***

081

MoveTo



Hide

076 VGA****

077_VGA****

078_VGA***

079_*****

080_PW_SKYLAKE-U (1)

081_PW_SKYLAKE-U (2)

082_PW_***

083_PW_+1.0VSUS/+1.8VSUS (6001)

084_PW_+1.5VS (UP0107)

085_PW_***

086_PW_+1.2V/+VTT/+2.5V (901
1)

087_PW_+3VADSW/+5VSUS

088_PW_LOAD SWITCH

089_PW_CHARGER(SN2867RUY
R)

090_PW_PROTECTION

091_PW_

092_PW_***

093 PW ***

094_PW_***

095_PW_***

096 PW/ ***

097 PW

009 DW *

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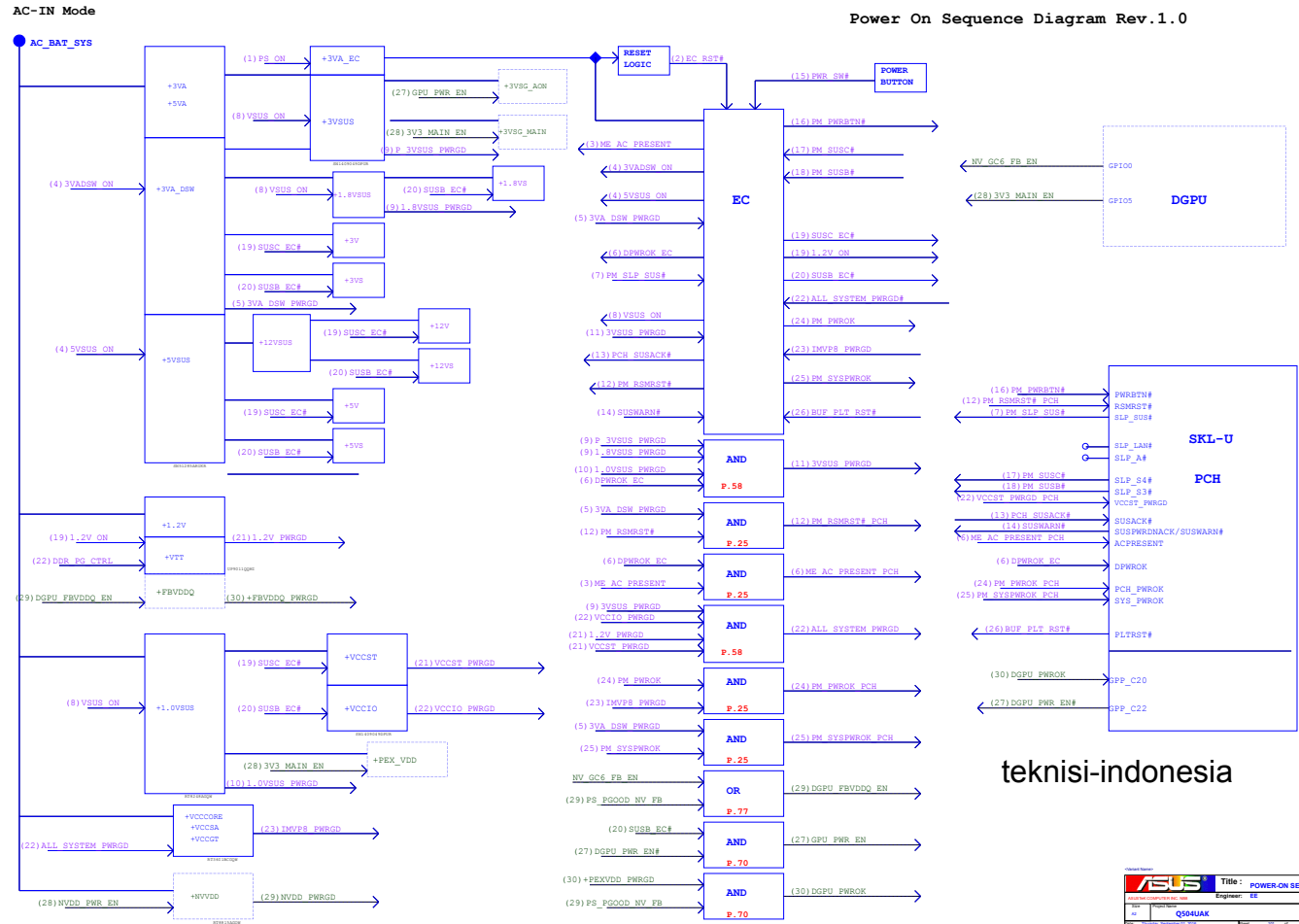
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Power On Sequence Diagram Rev.1.0



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